

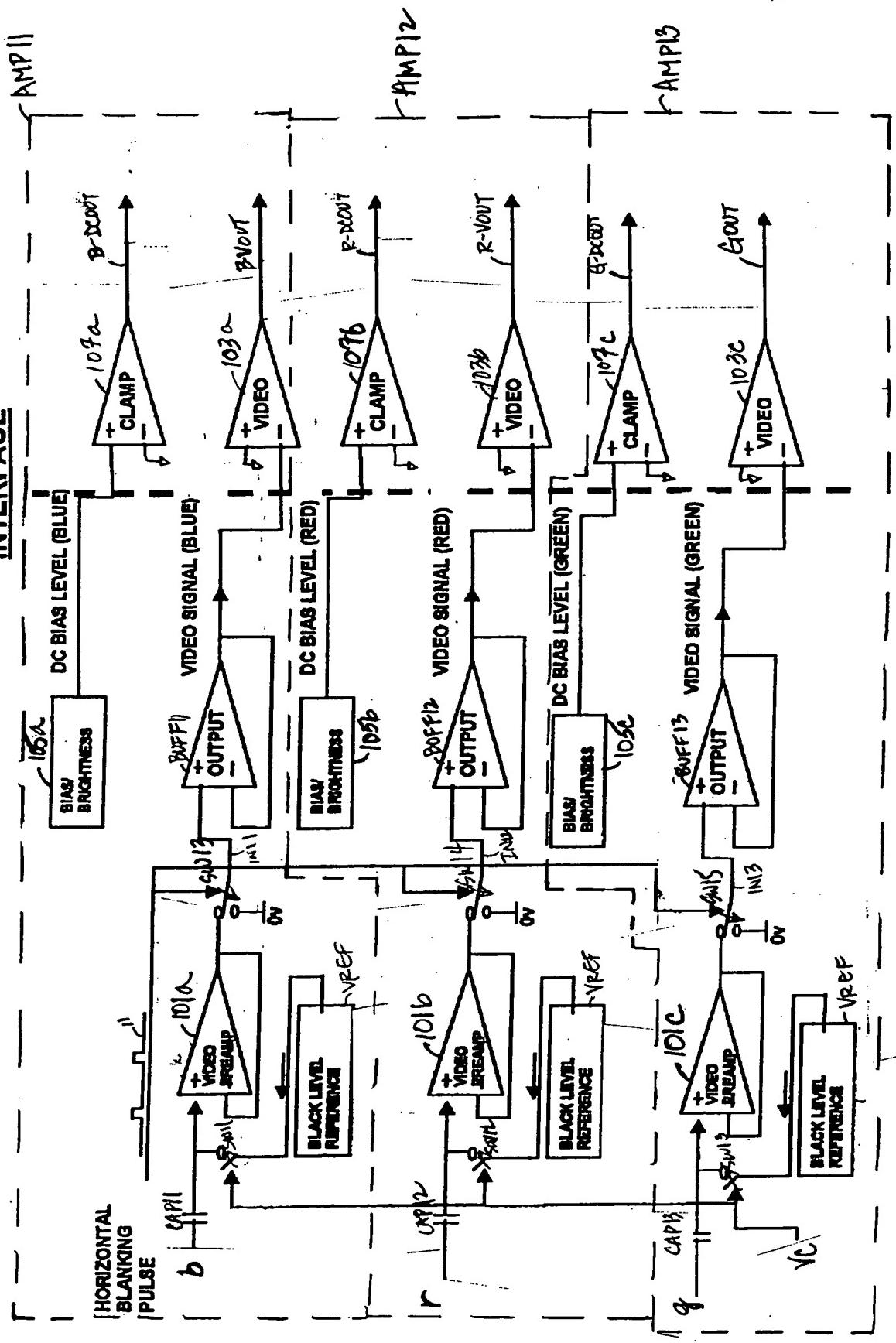
INTERFACE

FIGURE 1
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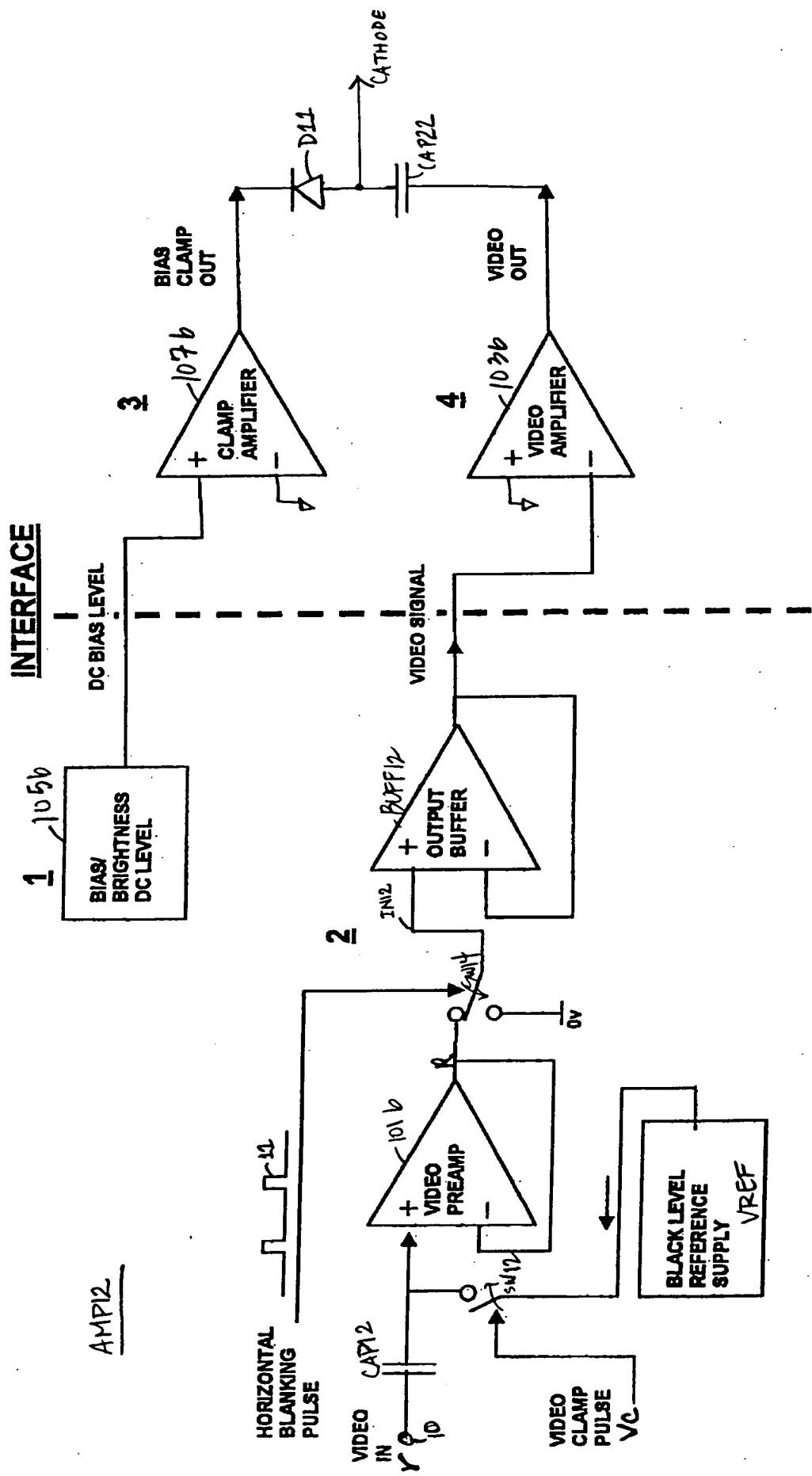


FIGURE 2

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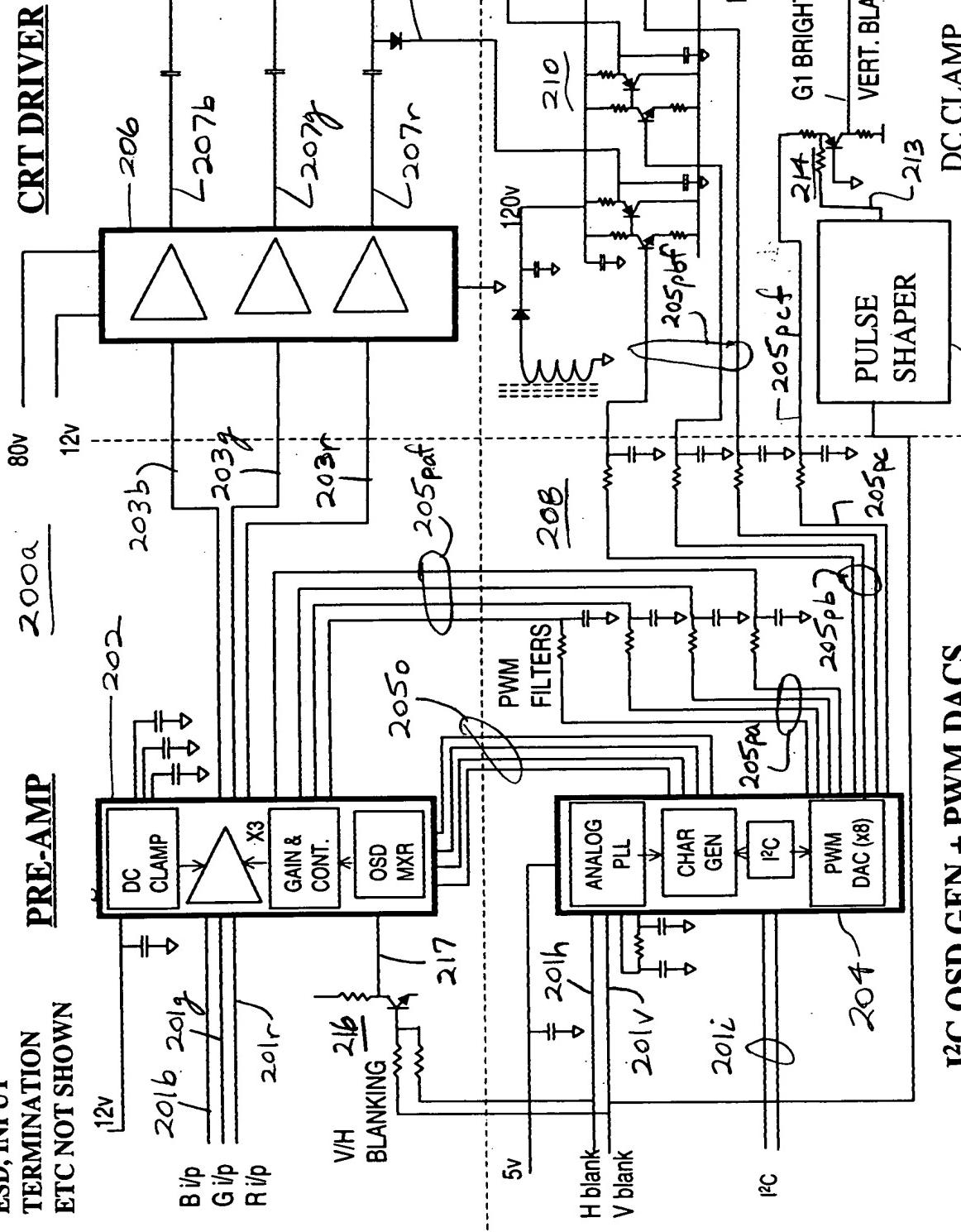
ARC PROTECTION,

ESD, INPUT

TERMINATION
ETC NOT SHOWN

DISPLAY VIDEO SYSTEM

CRT DRIVER



I²C OSD GEN + PWM DACS

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+ G1 DRIVE

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ARC PROTECTION,
ESD, INPUT
TERMINATION
ETC NOT SHOWN

DC COUPLED VIDEO SYSTEM

PRE-AMP

DC COUPLED CRT DRIVER

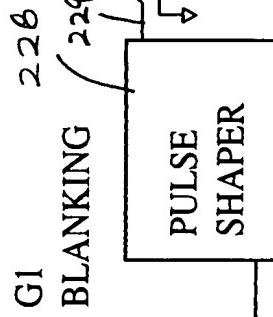
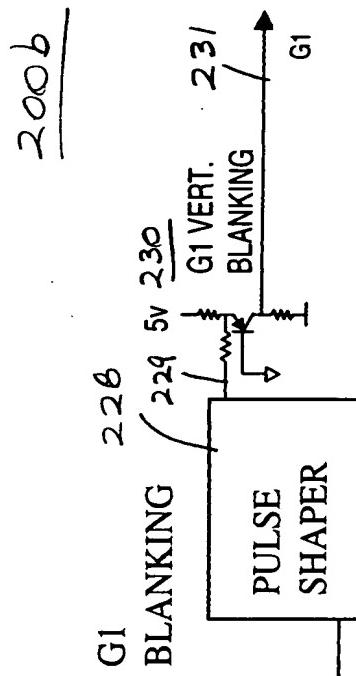
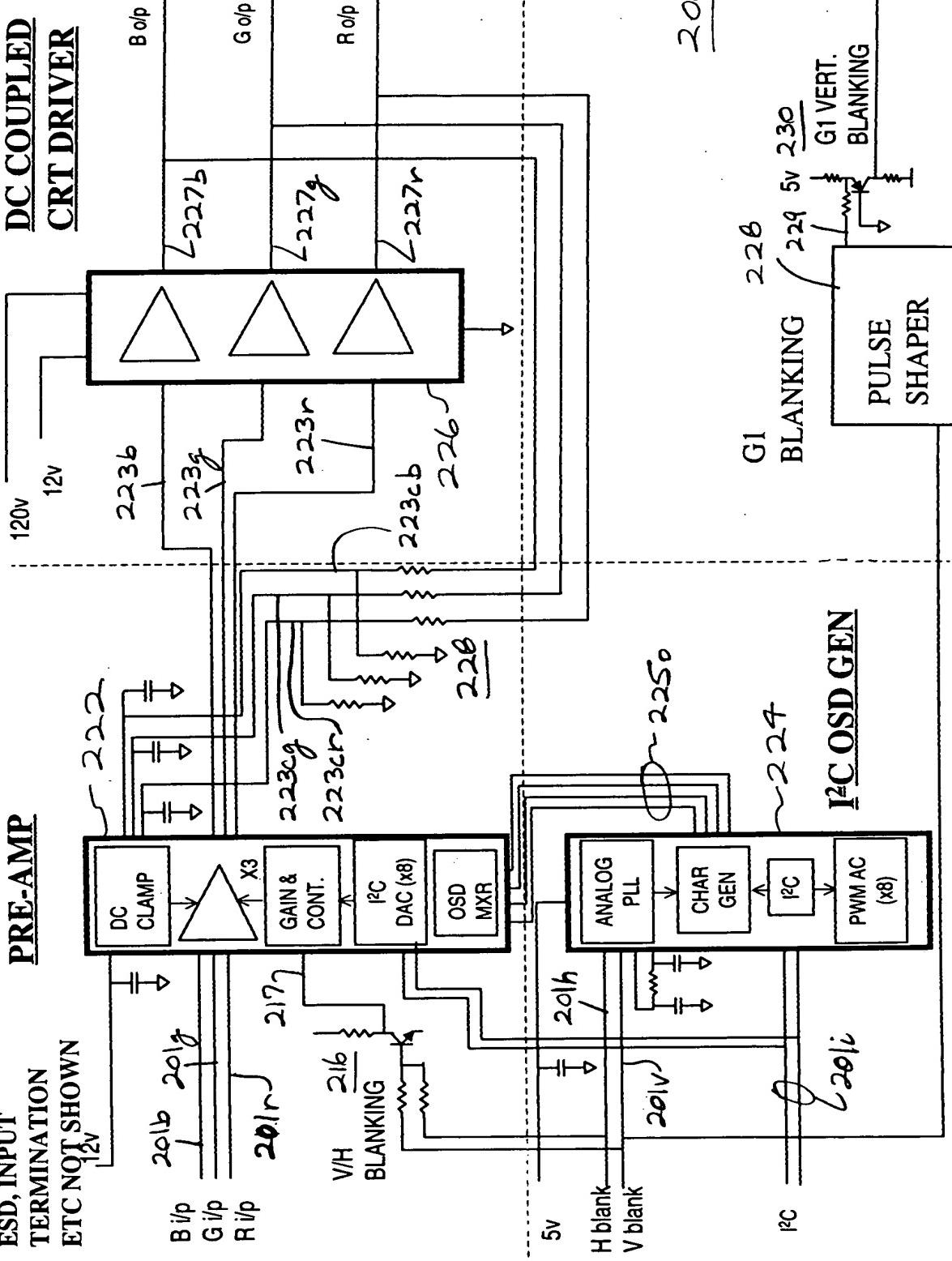
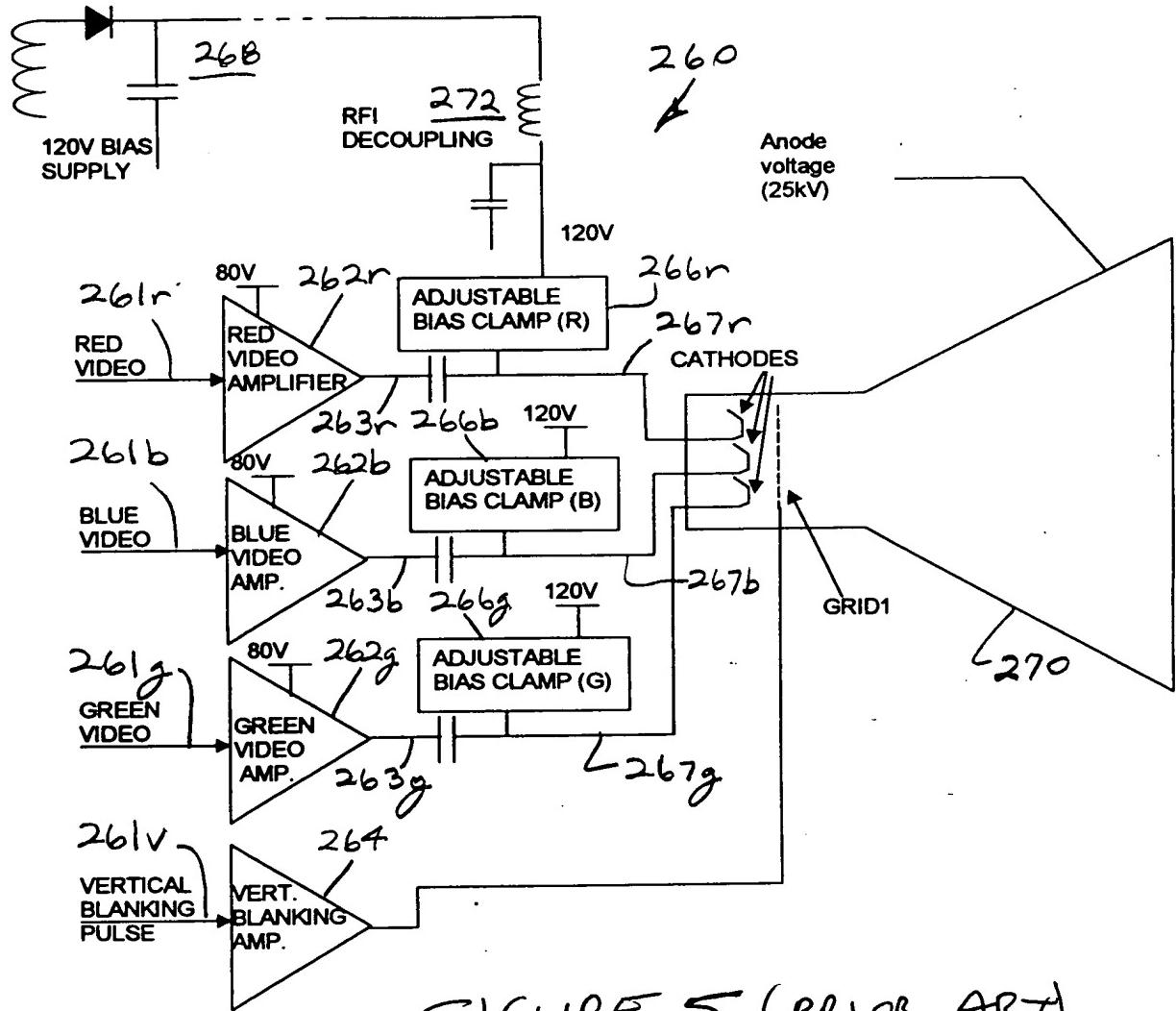


FIGURE 4 (PRIOR ART)



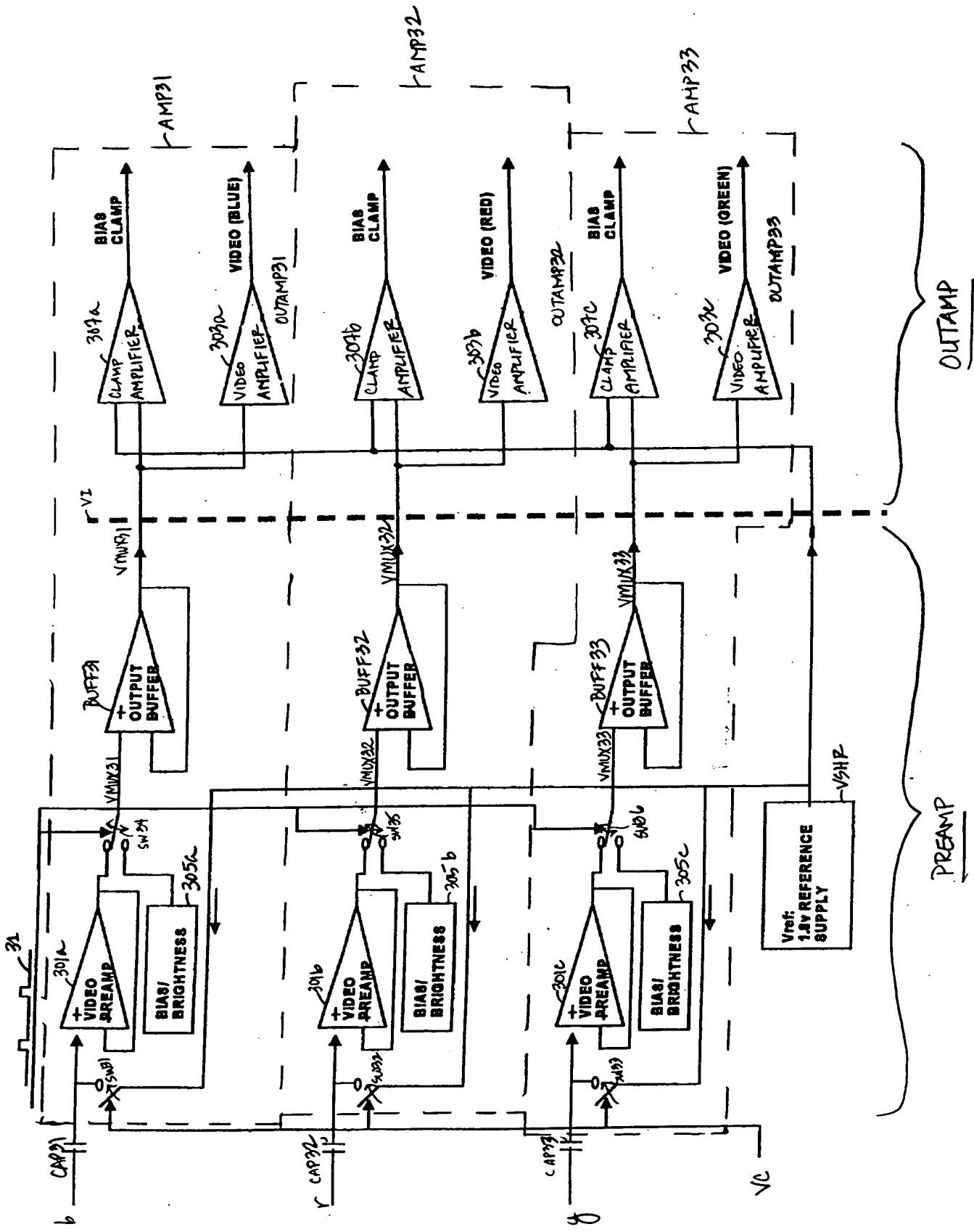


FIGURE 6

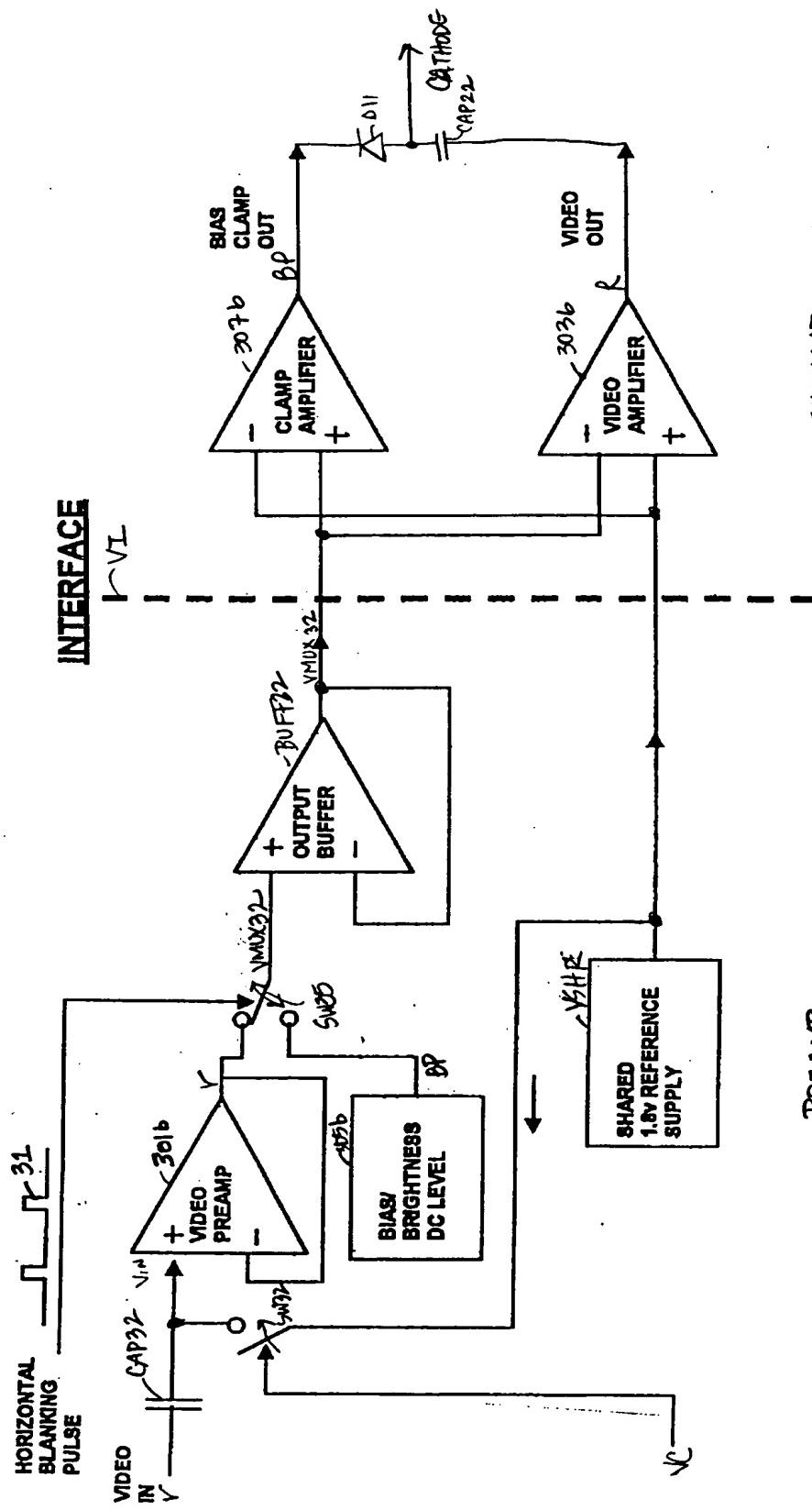
**OUTAMP****AMP32**

FIGURE 7

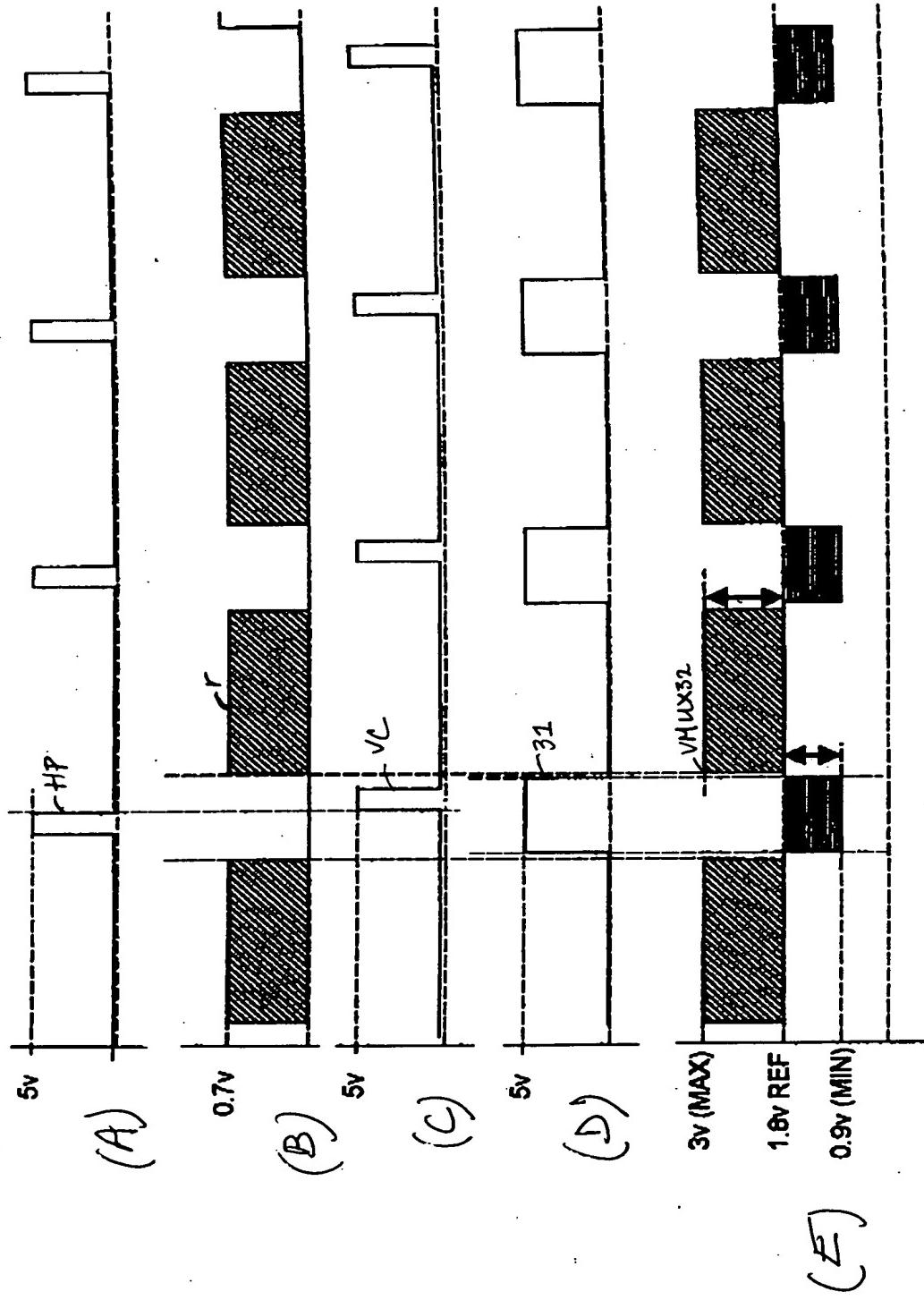


FIGURE 8

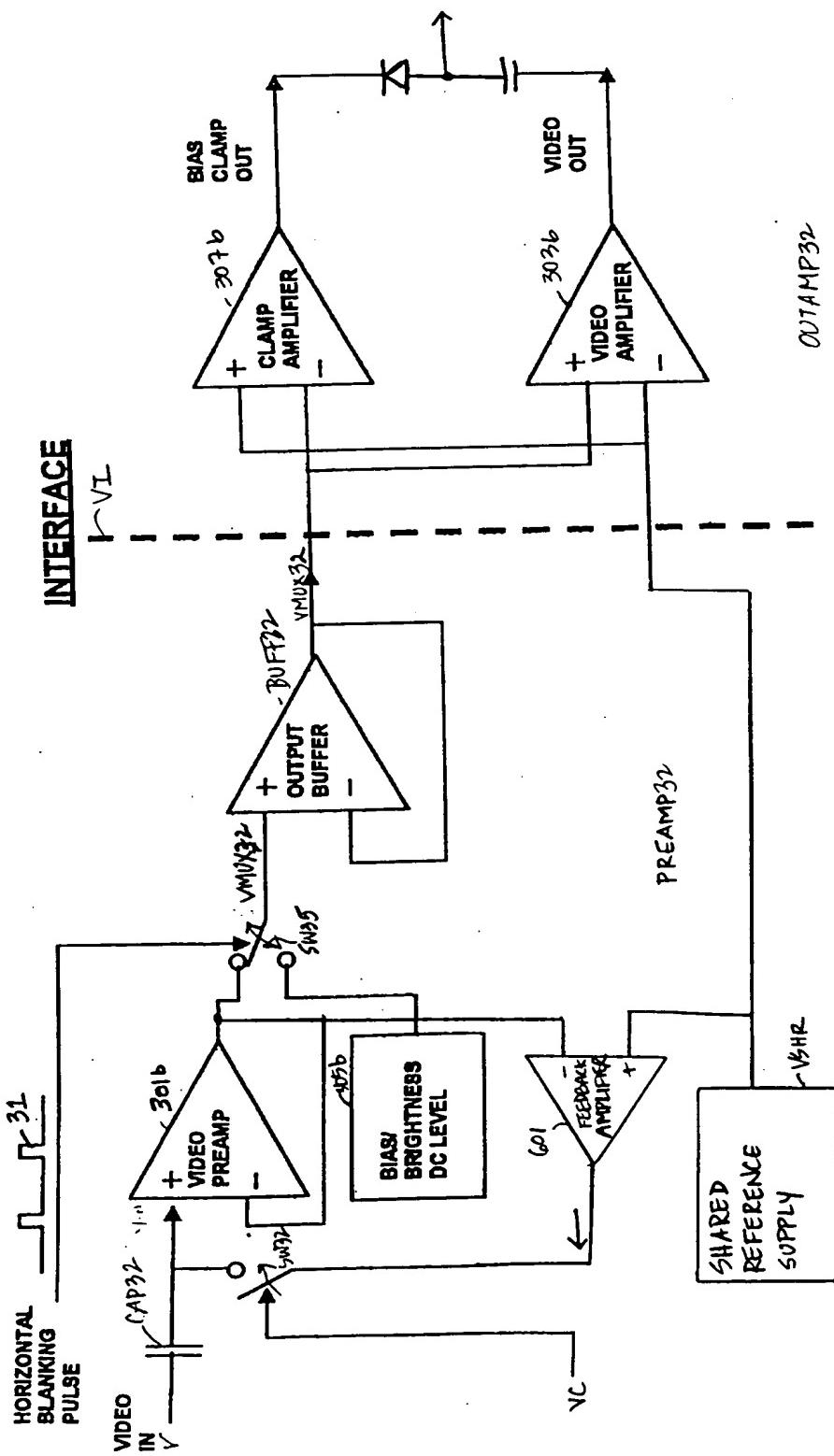


FIGURE 9

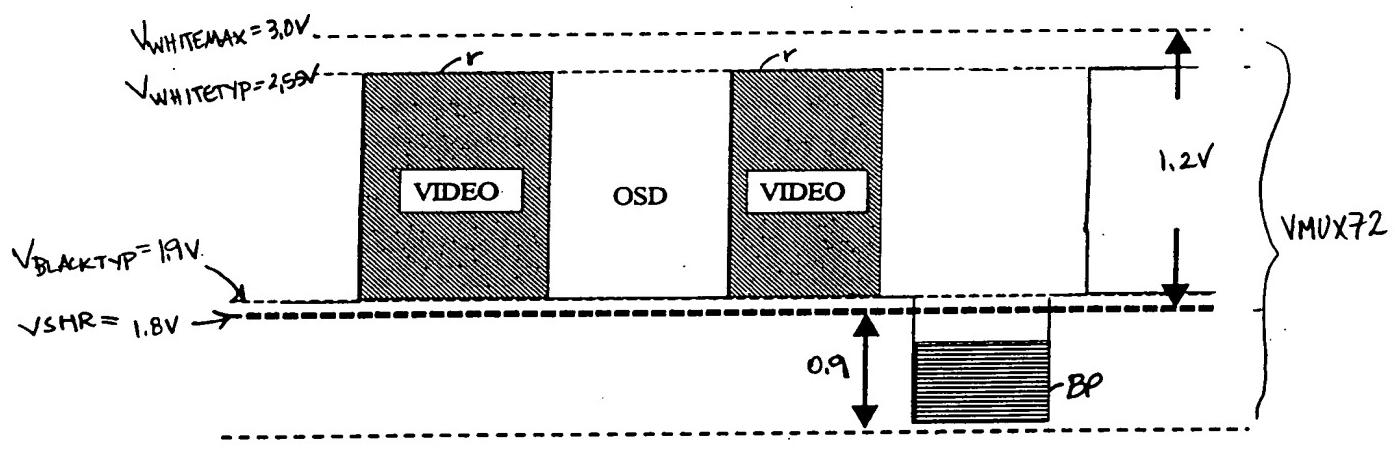


FIGURE 10

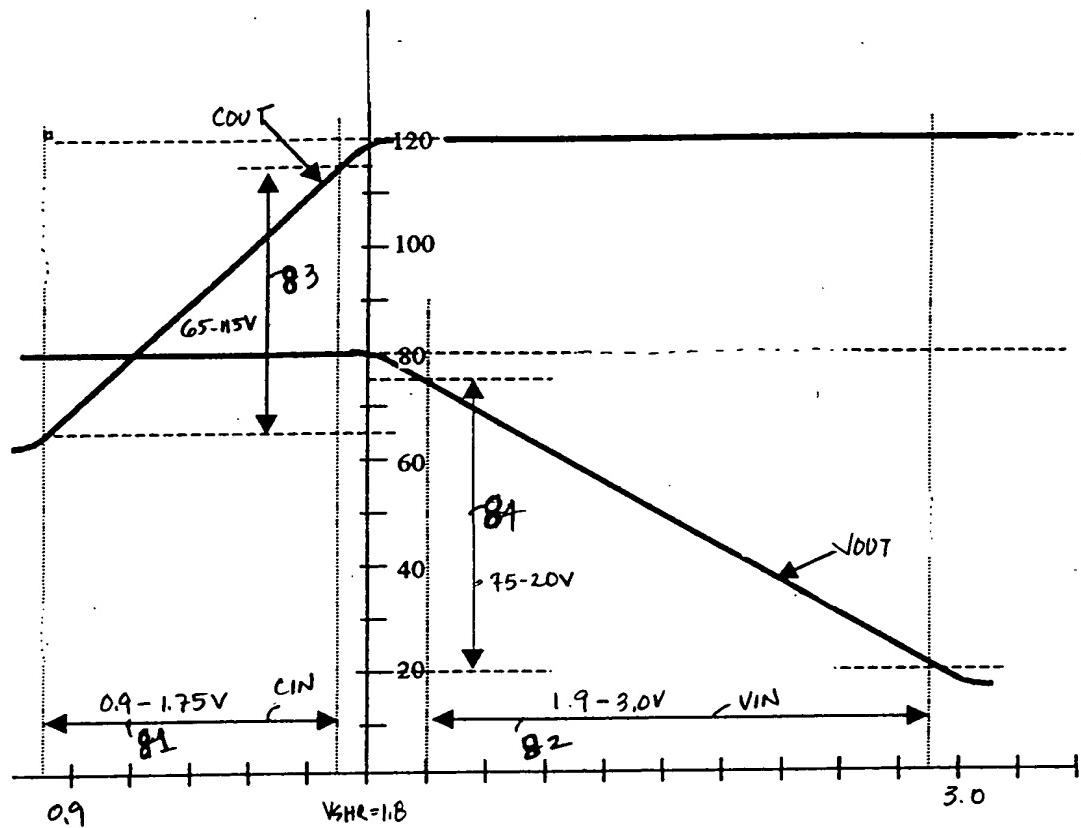


FIGURE 11

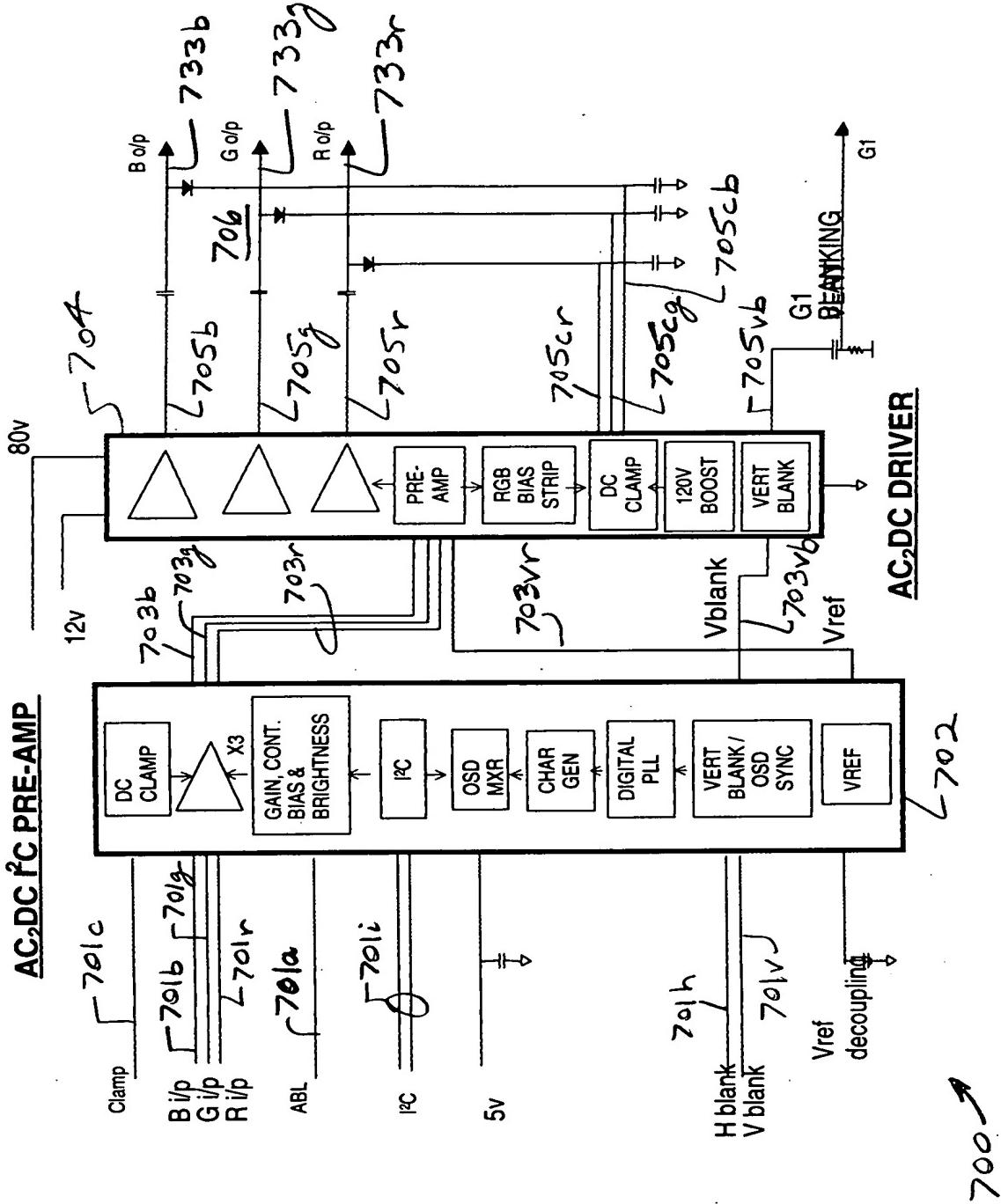


FIGURE 12

700 →

702

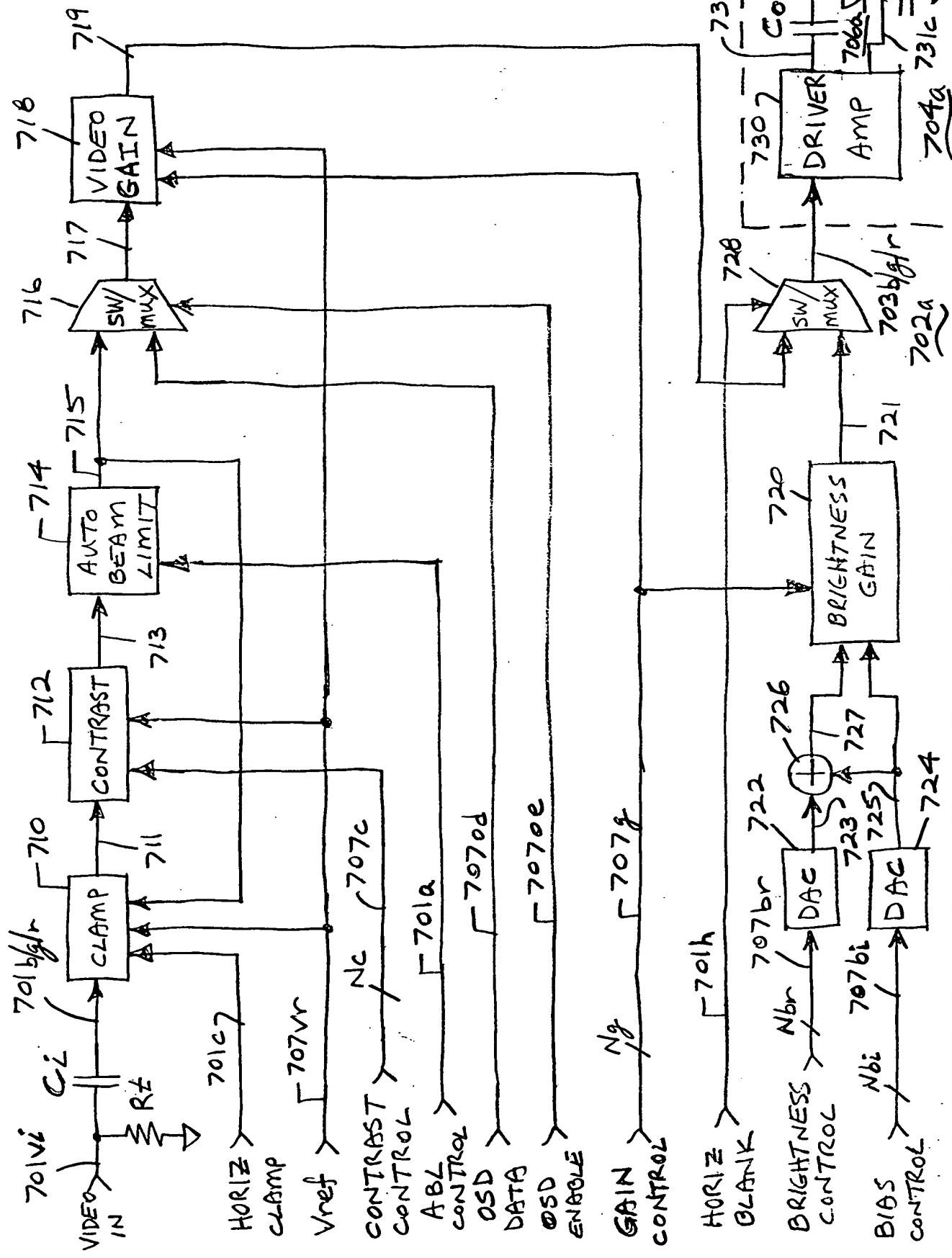


FIGURE 13



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INPUT SIGNALS



OUTPUT SIGNAL



V_{black}

V_{ref}

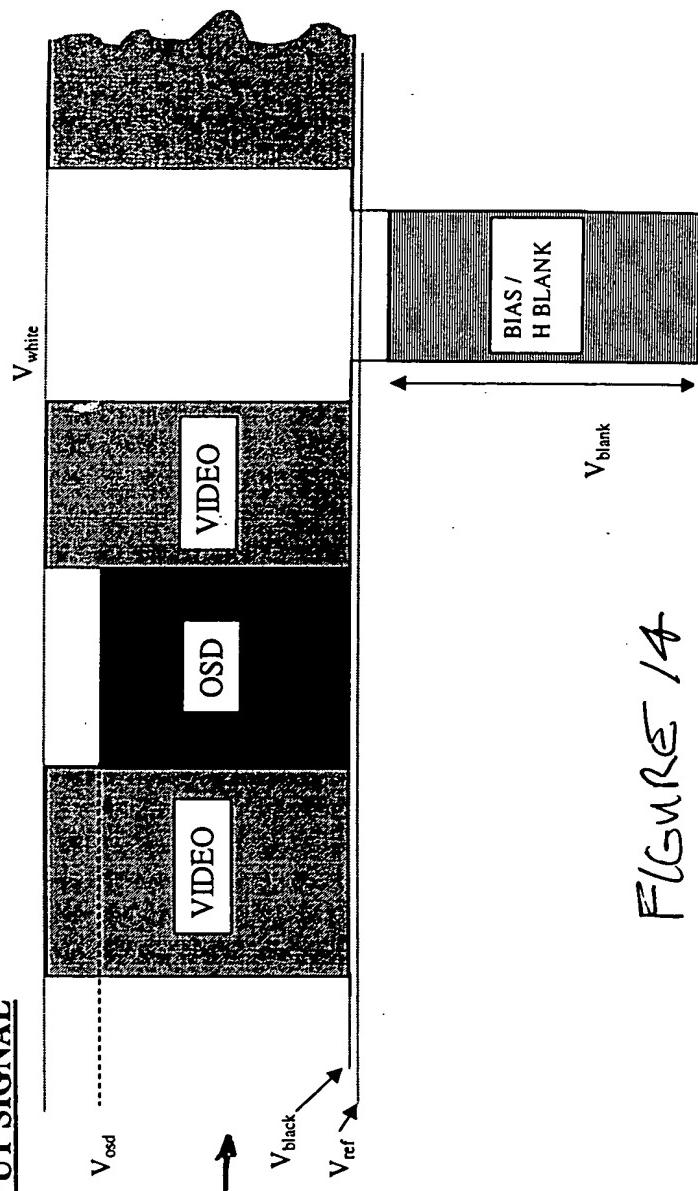


FIGURE 14



National Semiconductor

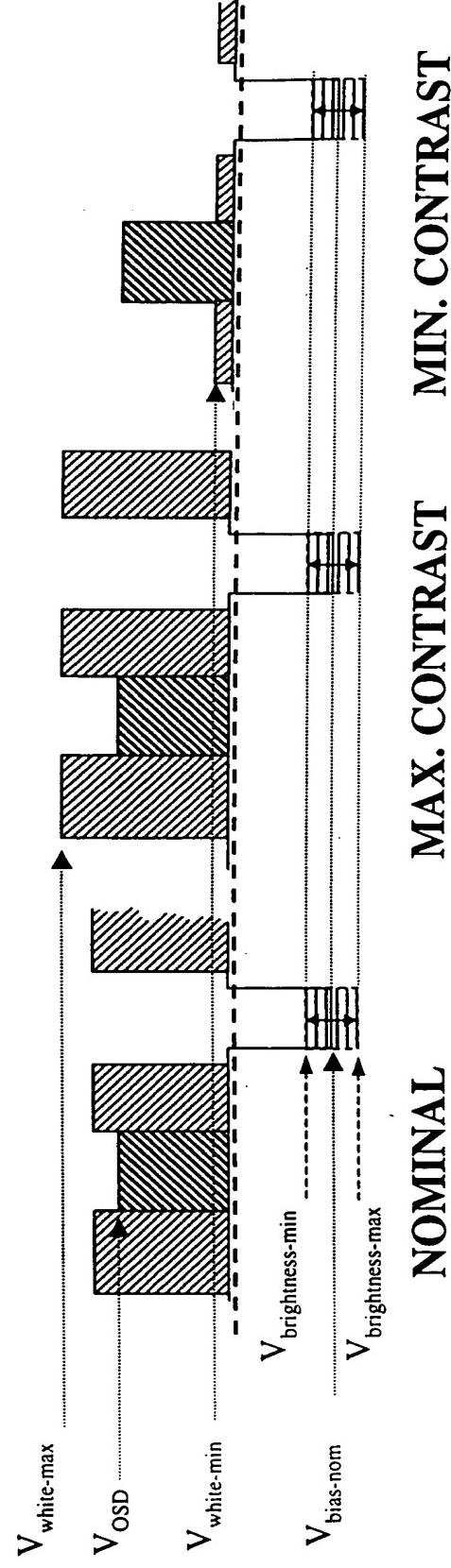


FIGURE 15



National Semiconductor

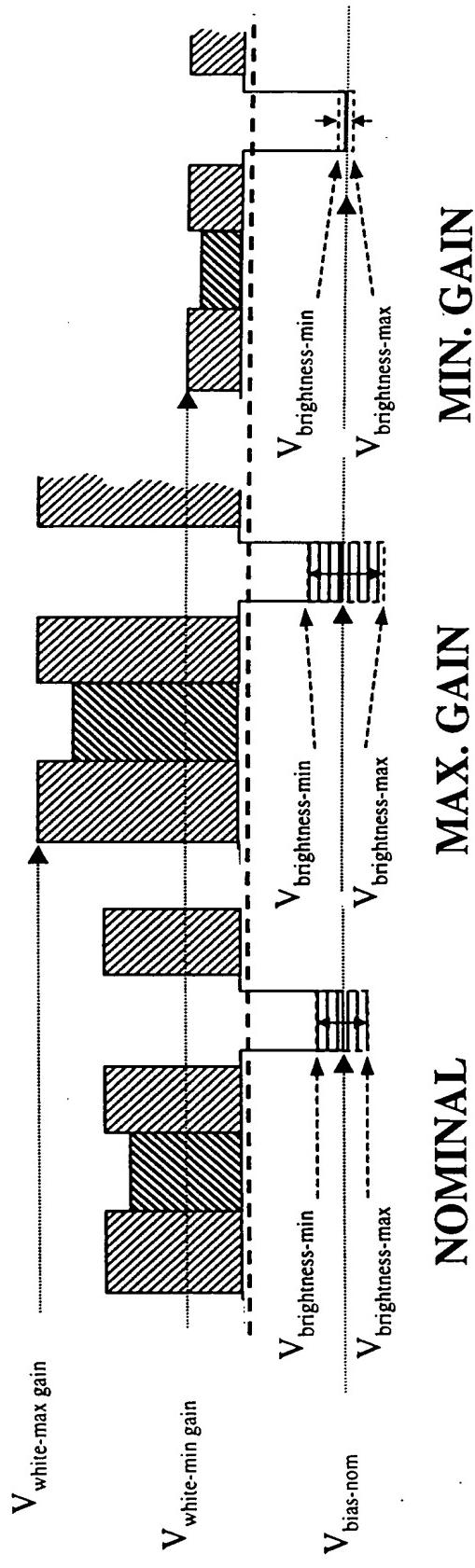


FIGURE 16



National Semiconductor

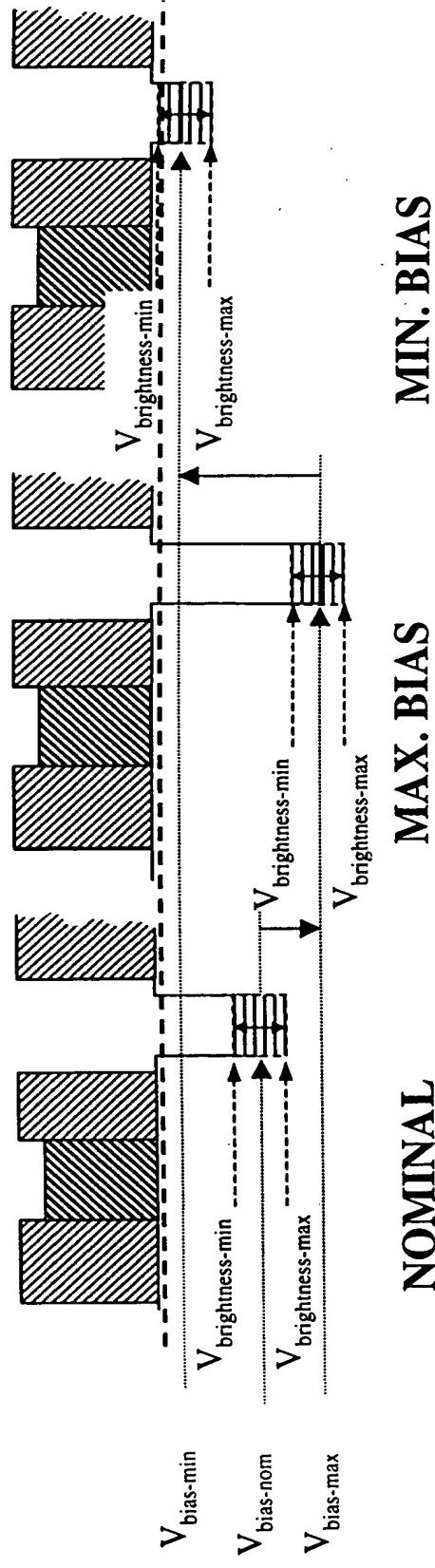


FIGURE 17

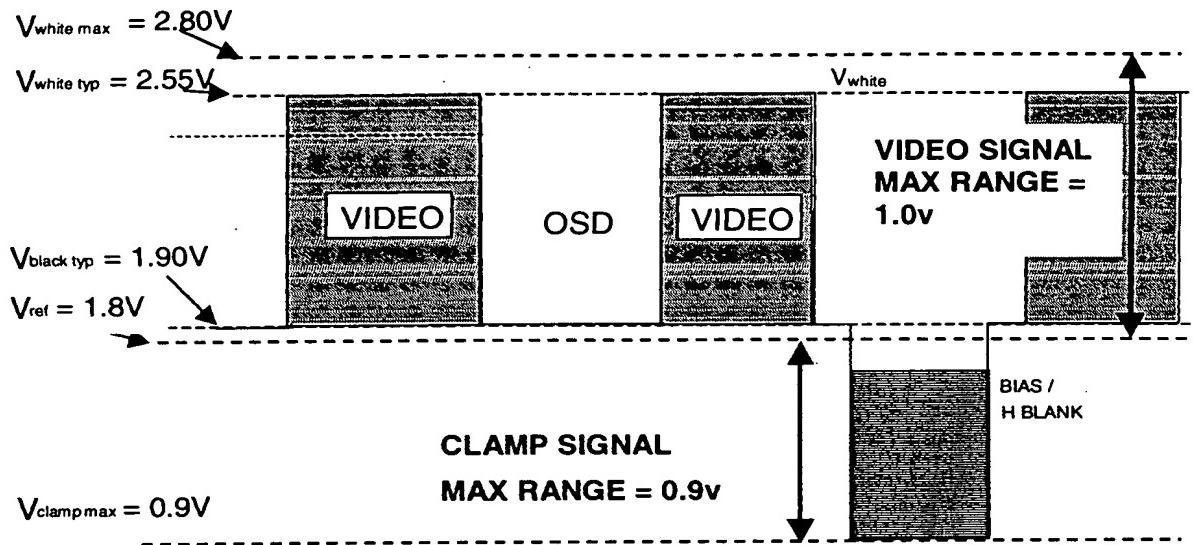


FIGURE 18

703 b/g/r

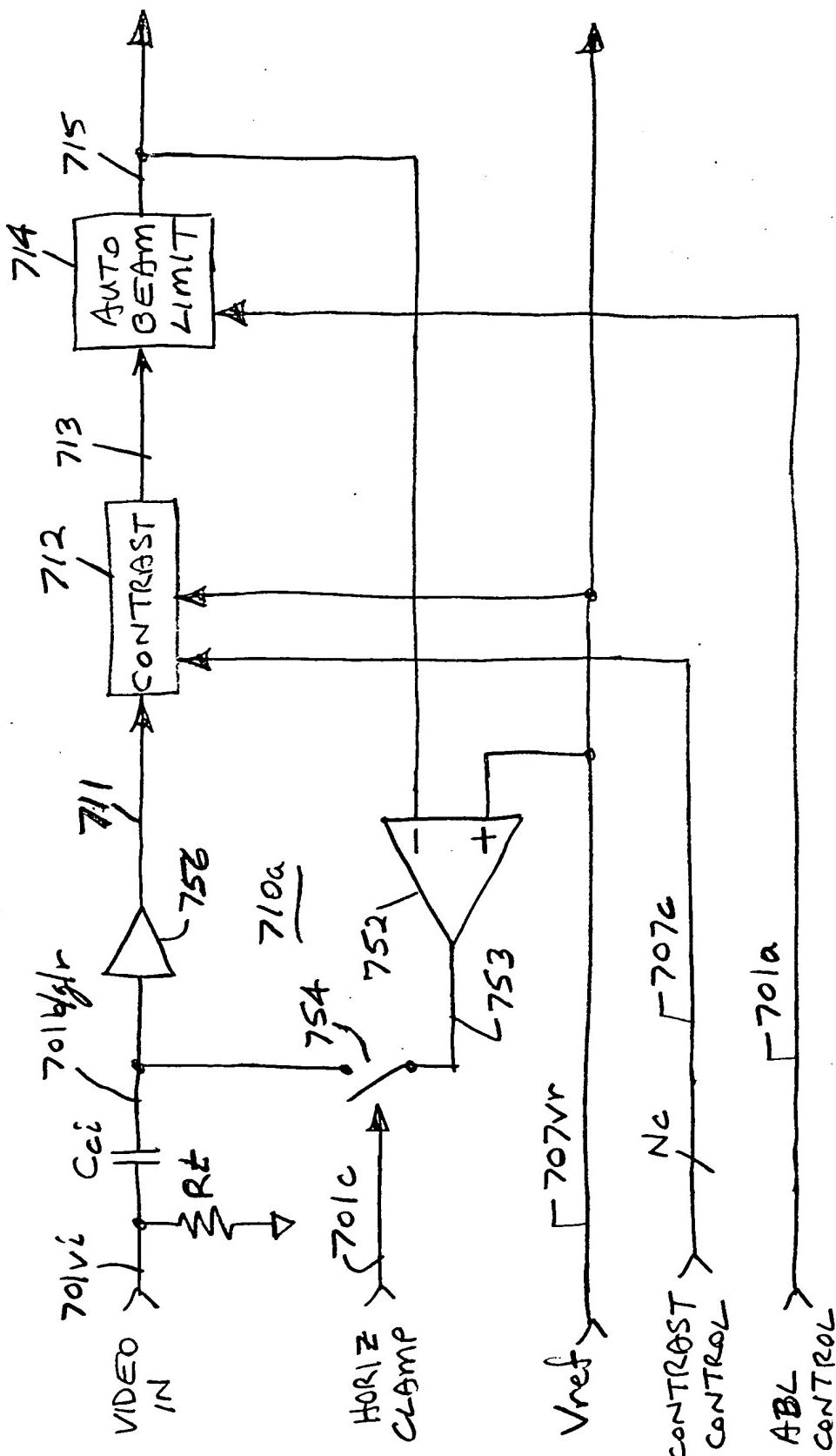


FIGURE 19

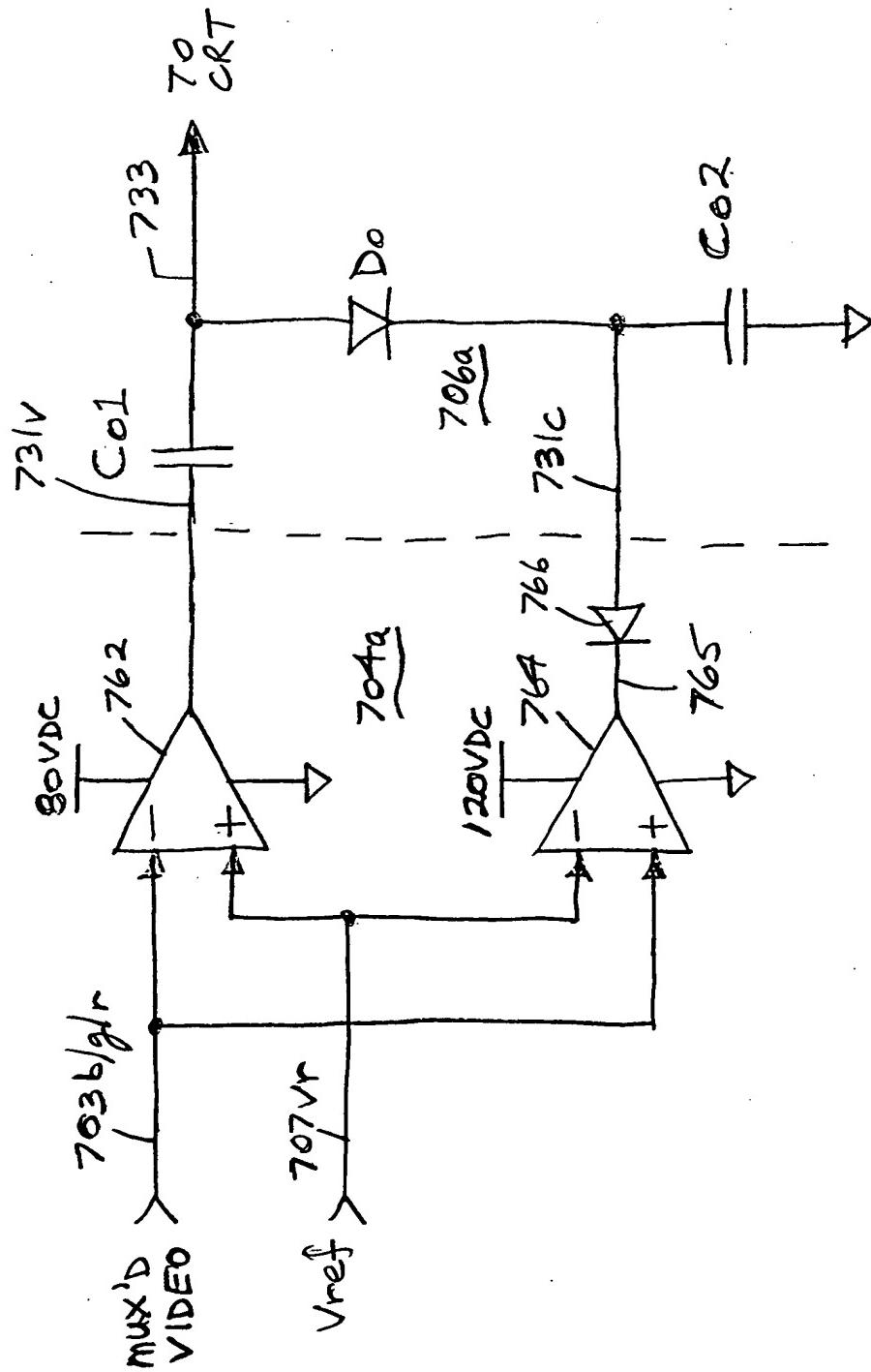
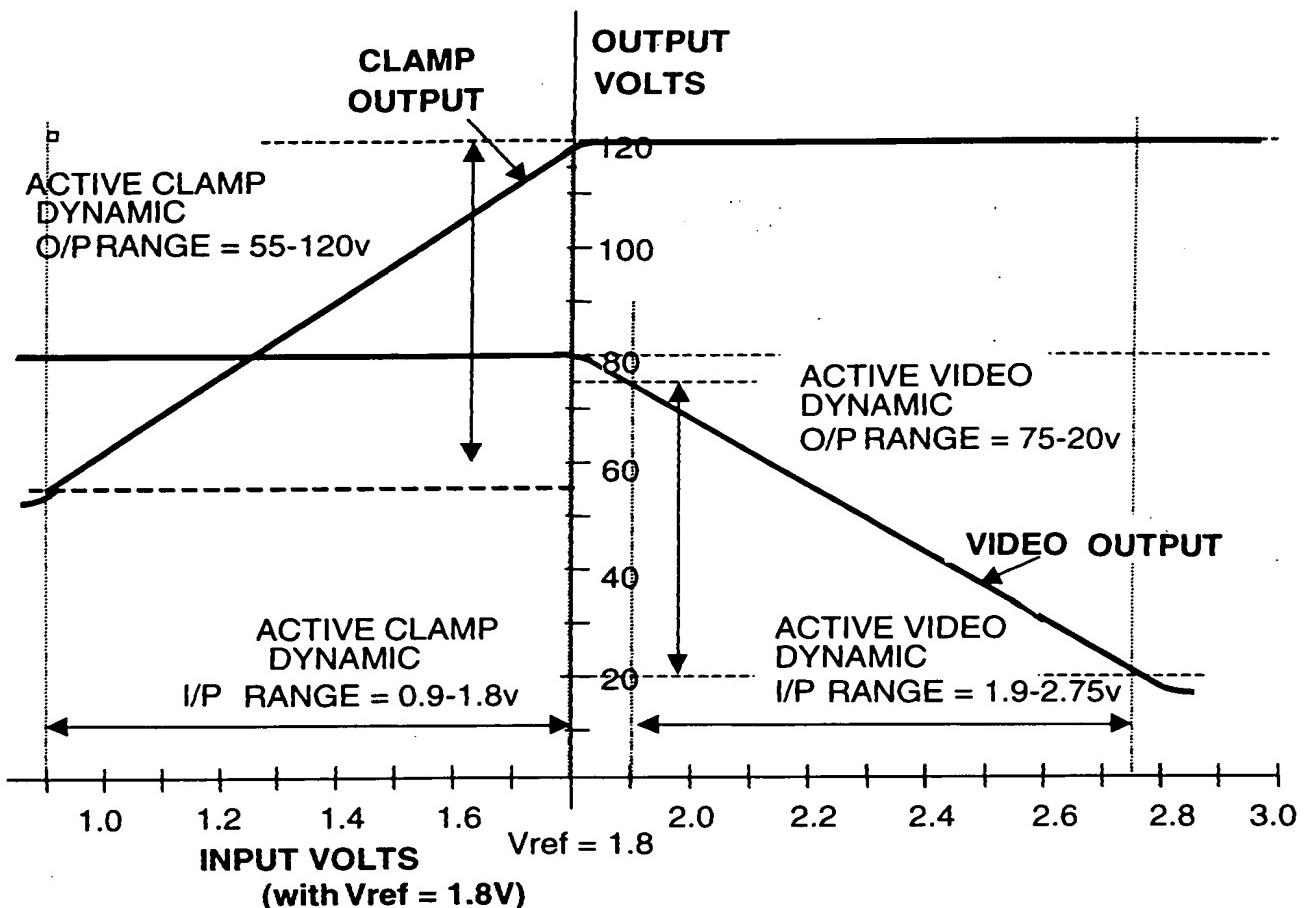


FIGURE 20



(Test Conditions: $V_{ref} = 1.8v$, $V_{cc1} = 80v$, $V_{cc2} = 120v$, $V_{bb} = 12v$)

FIGURE 21

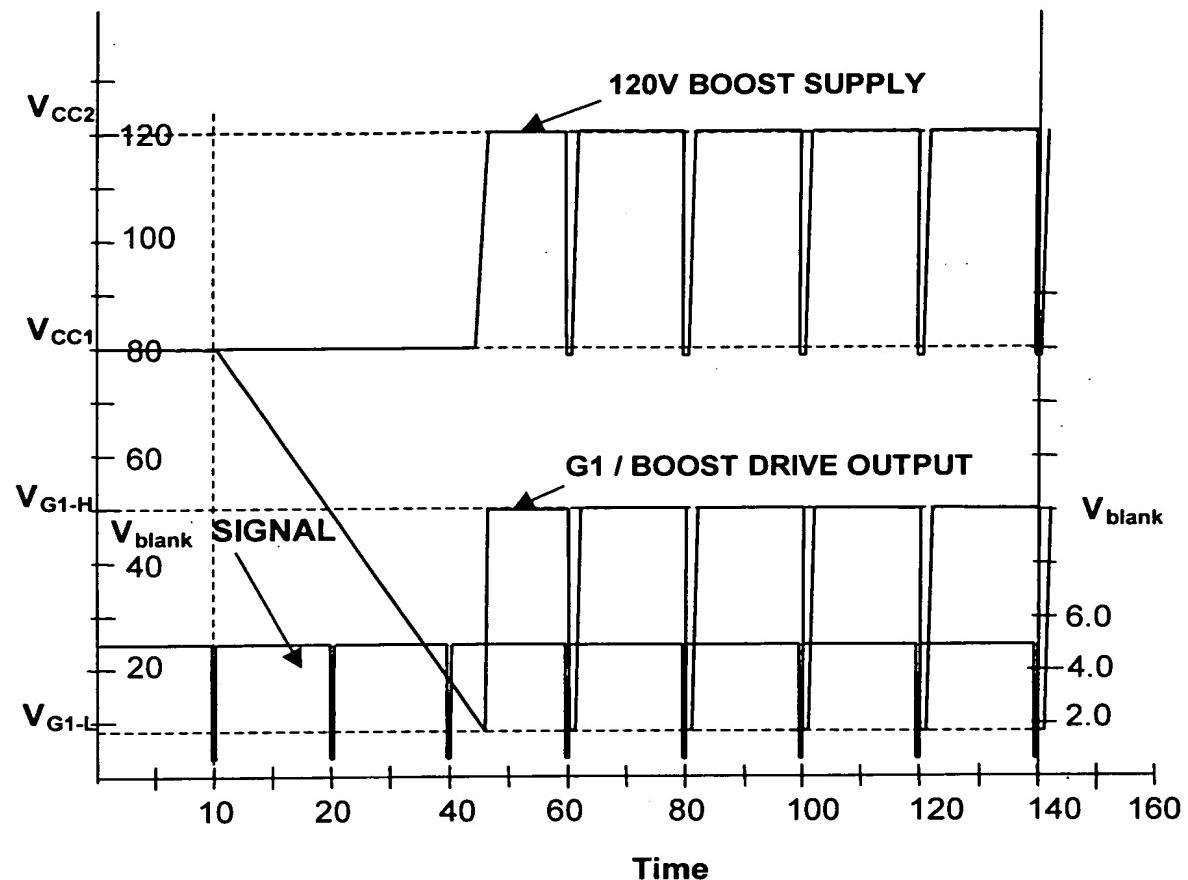


FIGURE 22

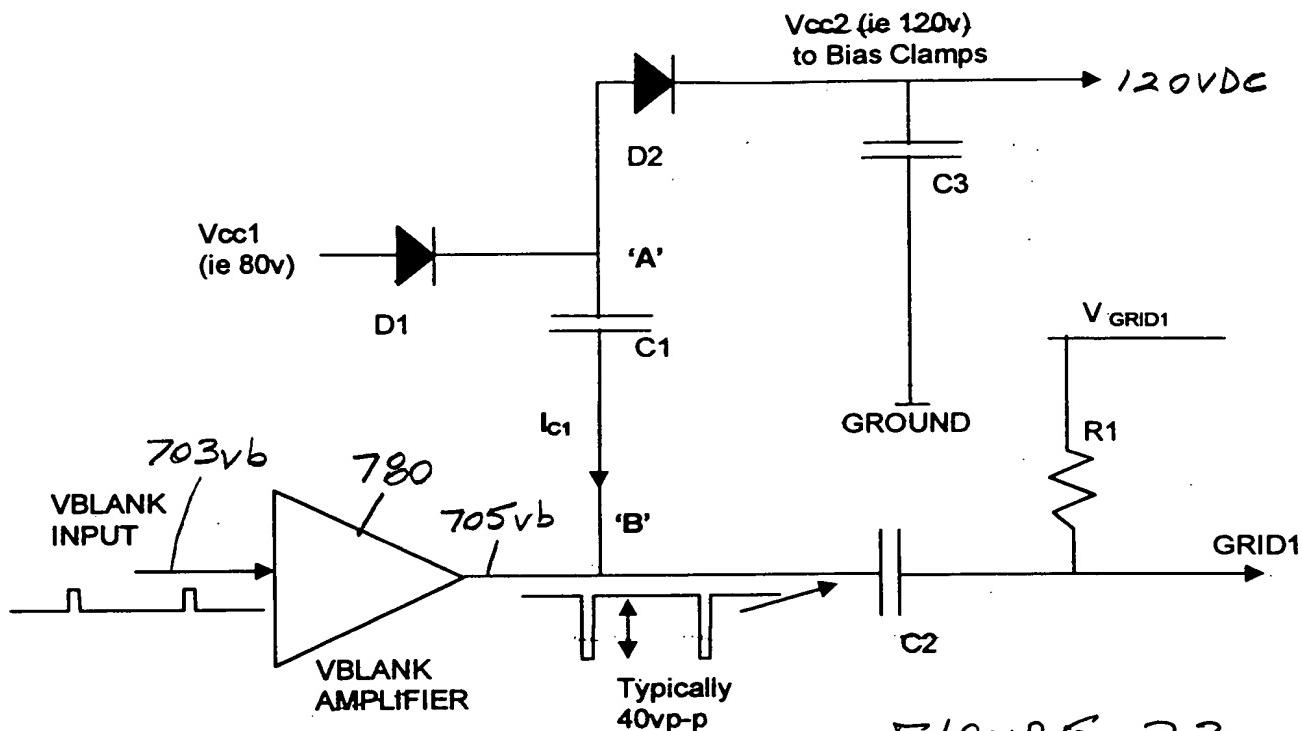


FIGURE 23

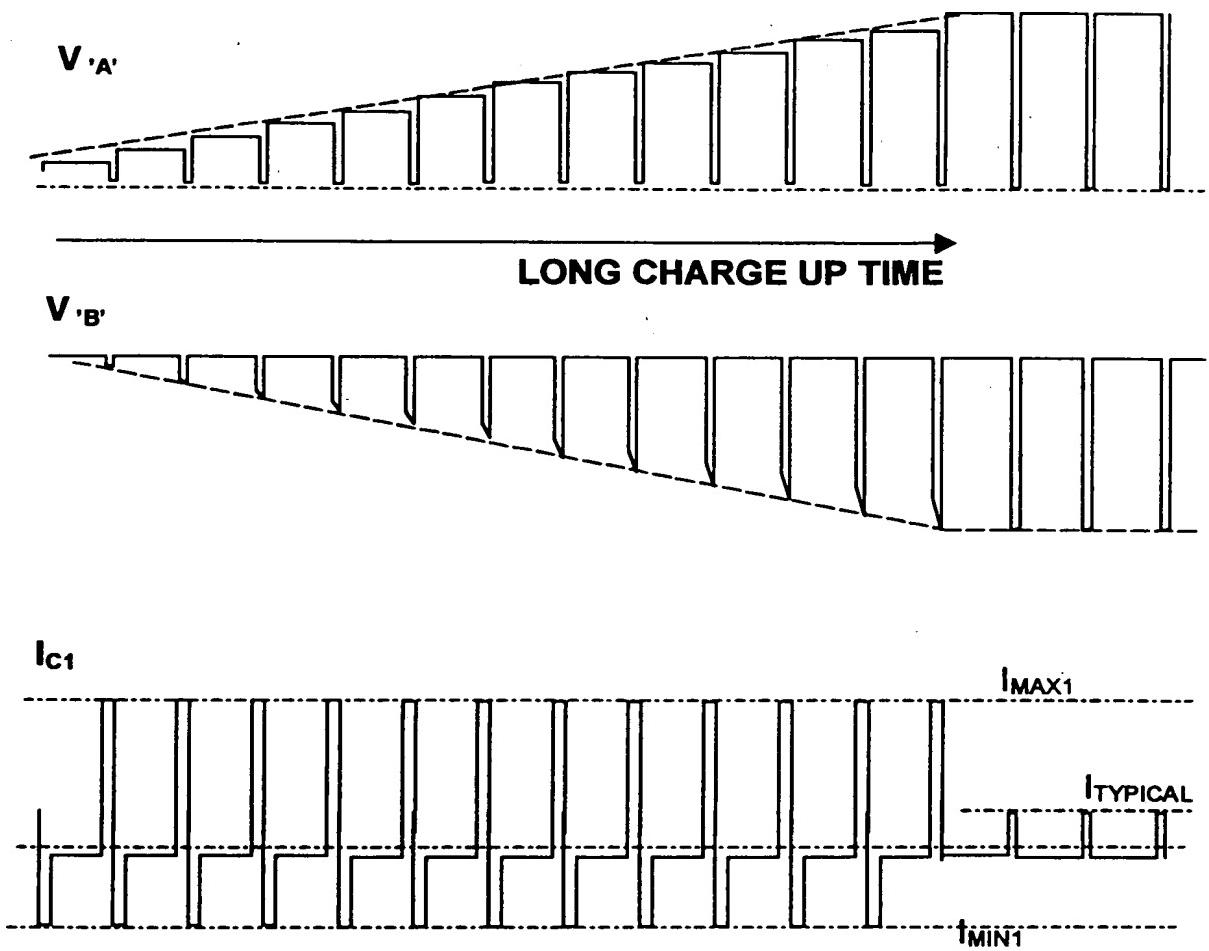


FIGURE 24

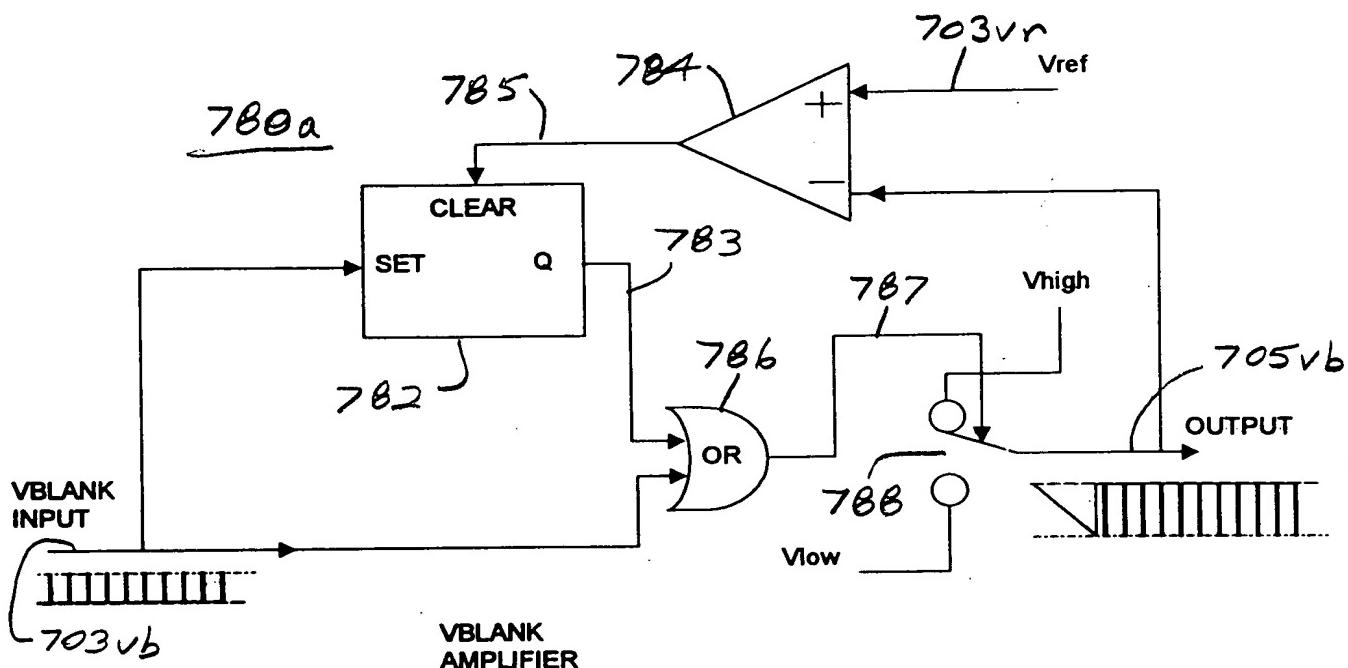


FIGURE 25

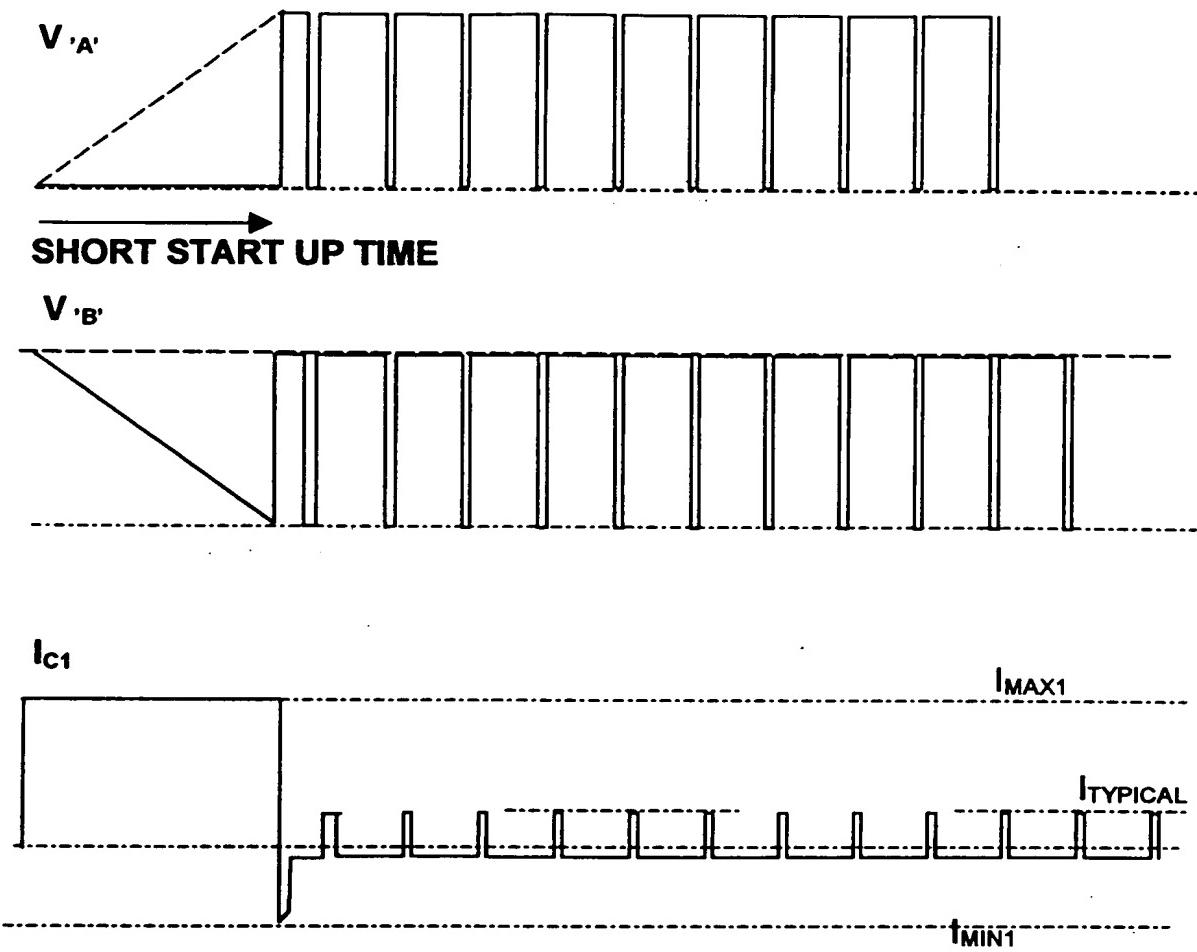


FIGURE 26

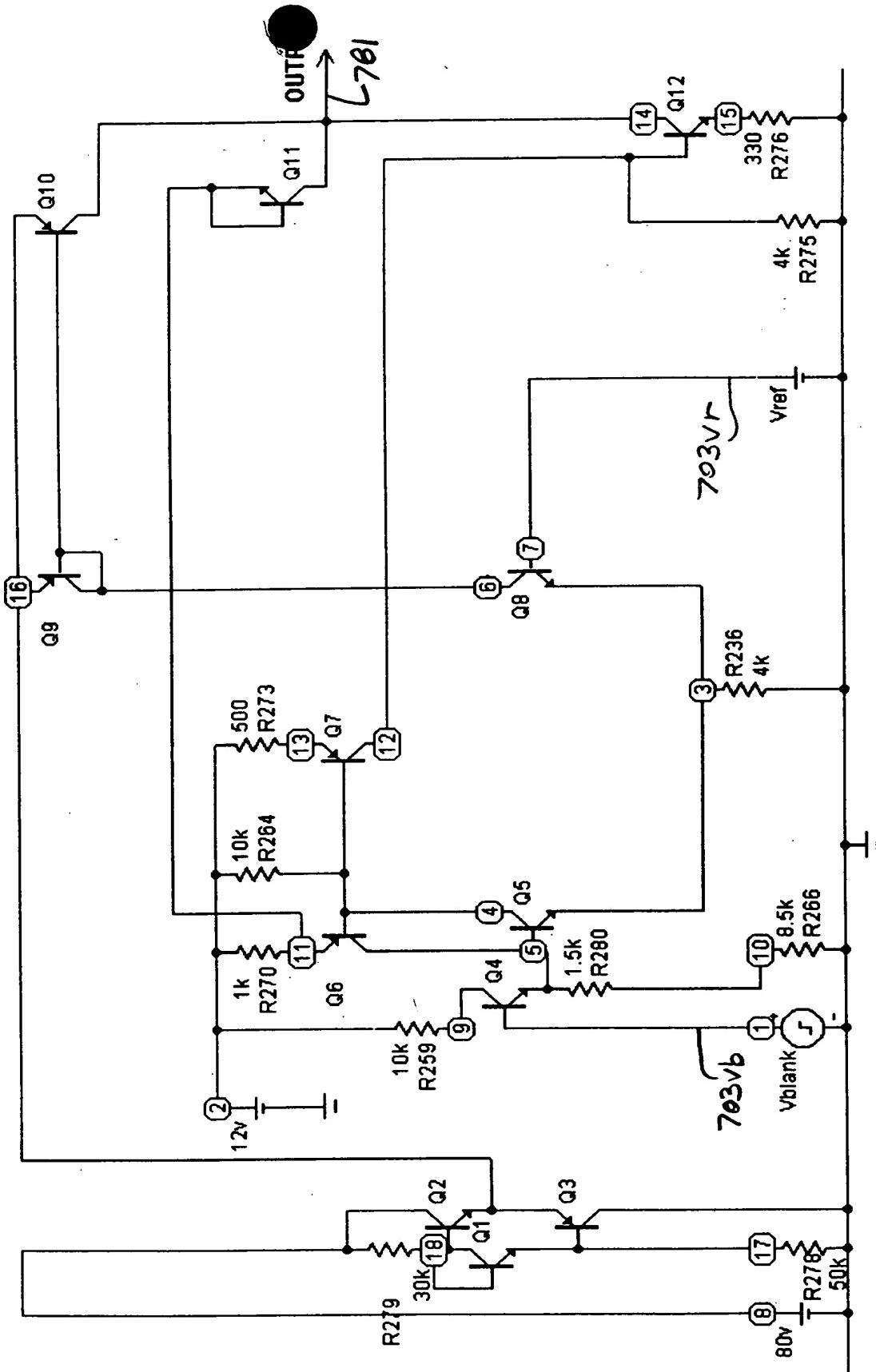


FIGURE 27

700b

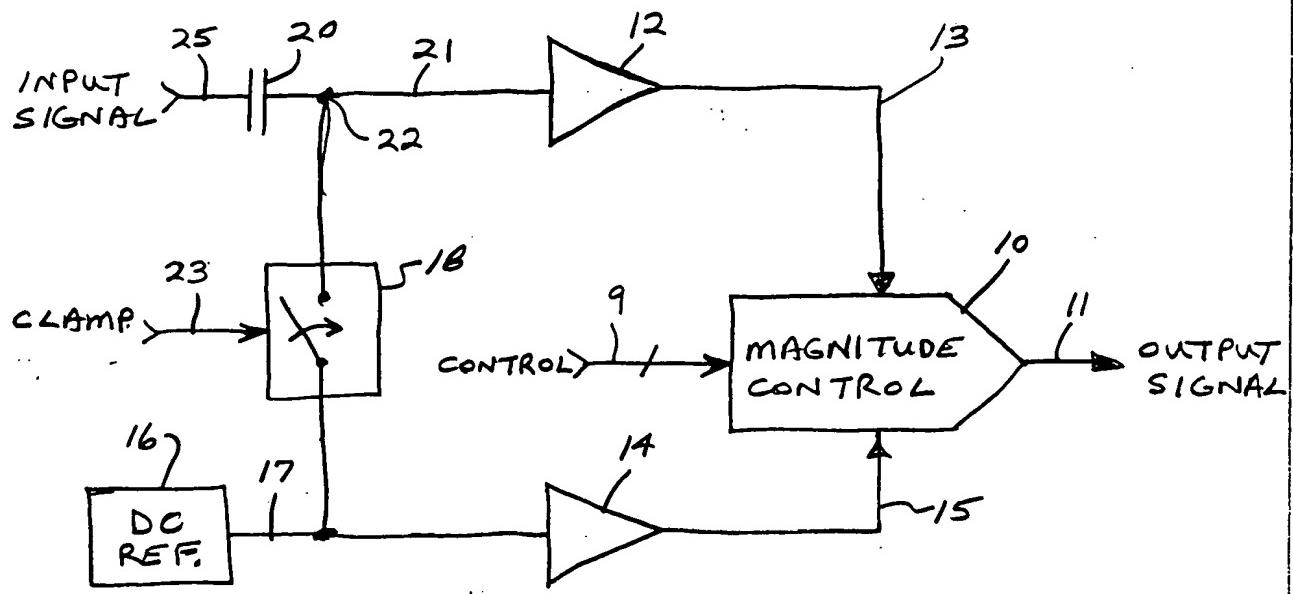


FIGURE 28

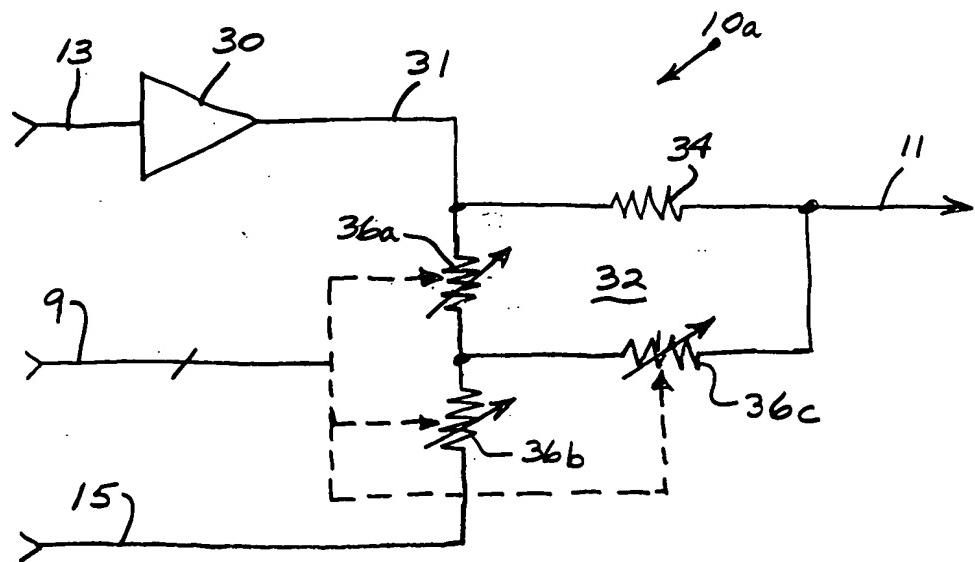


FIGURE 29

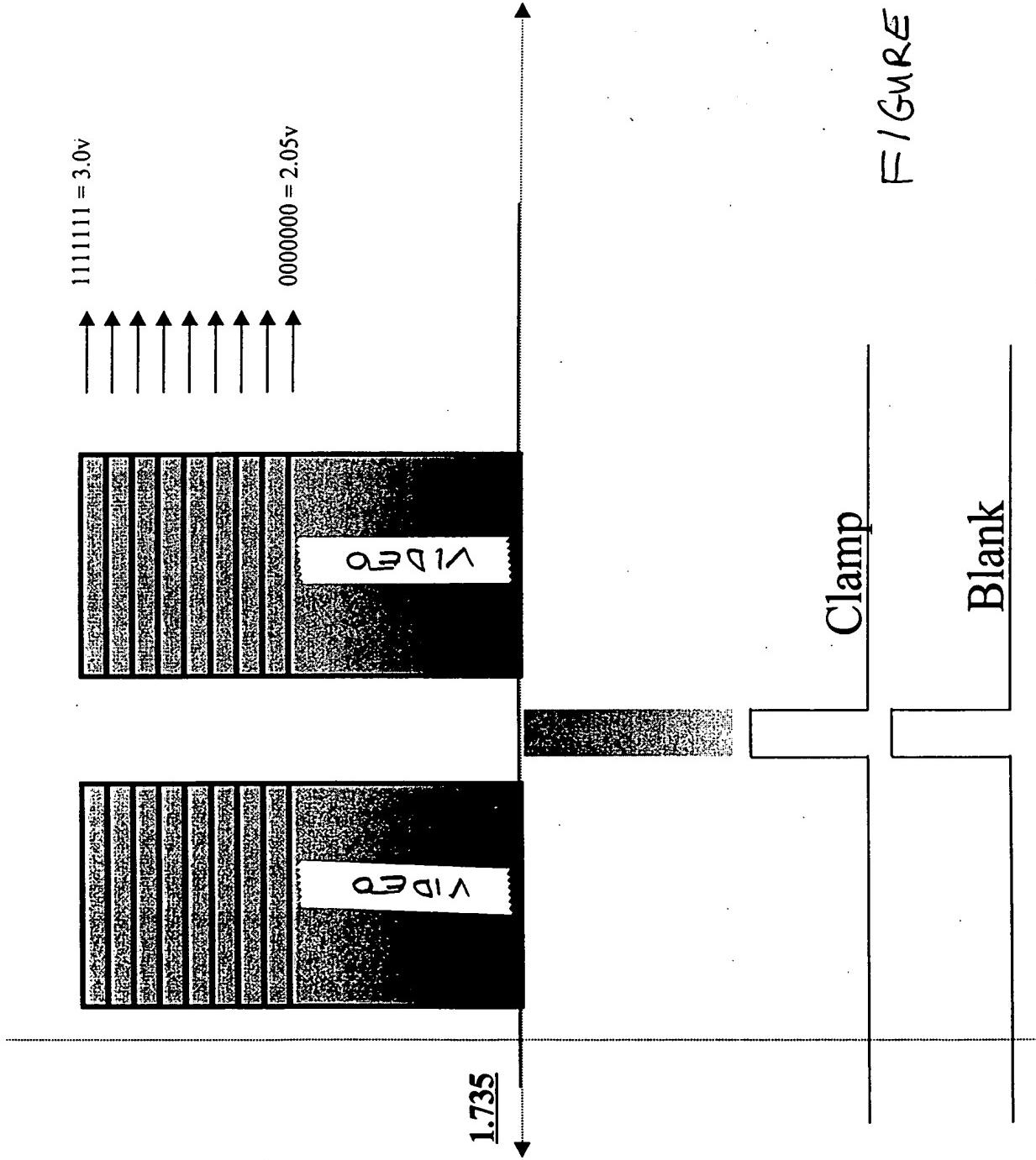


FIGURE 30

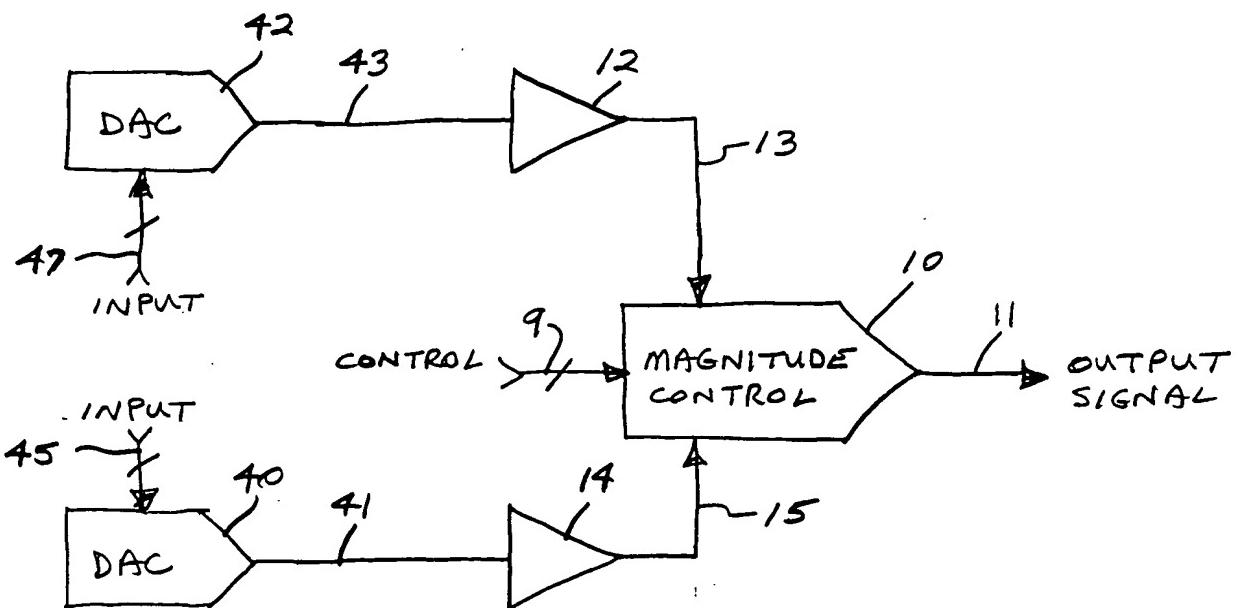


FIGURE 31

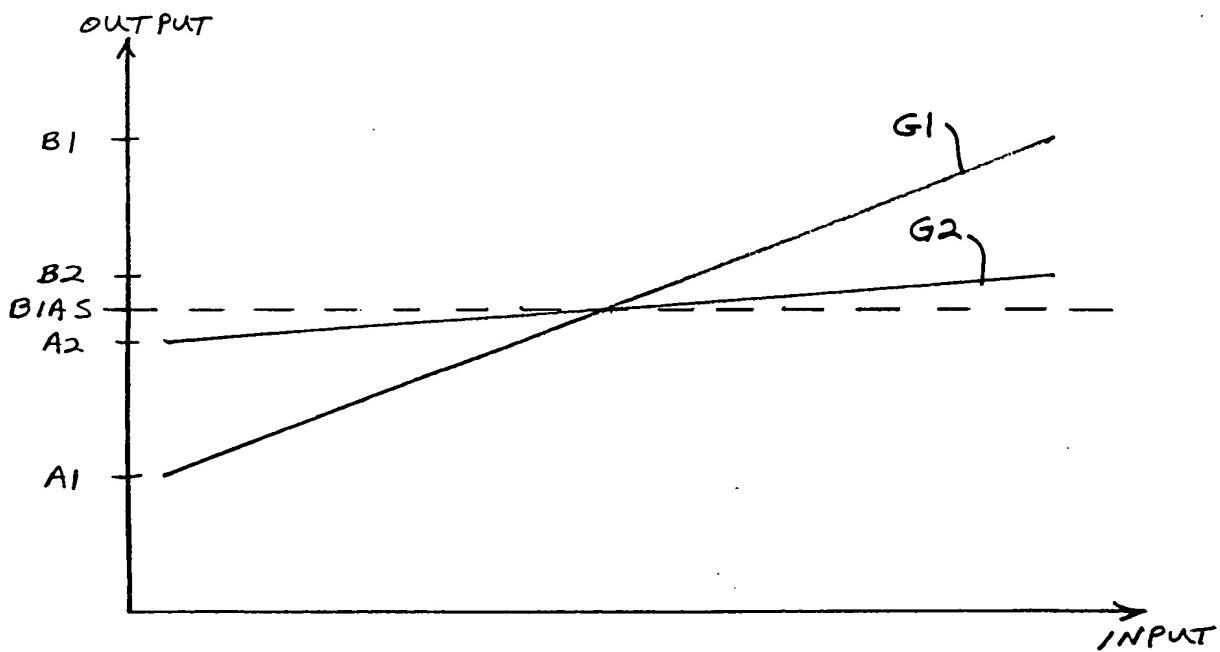


FIGURE 32

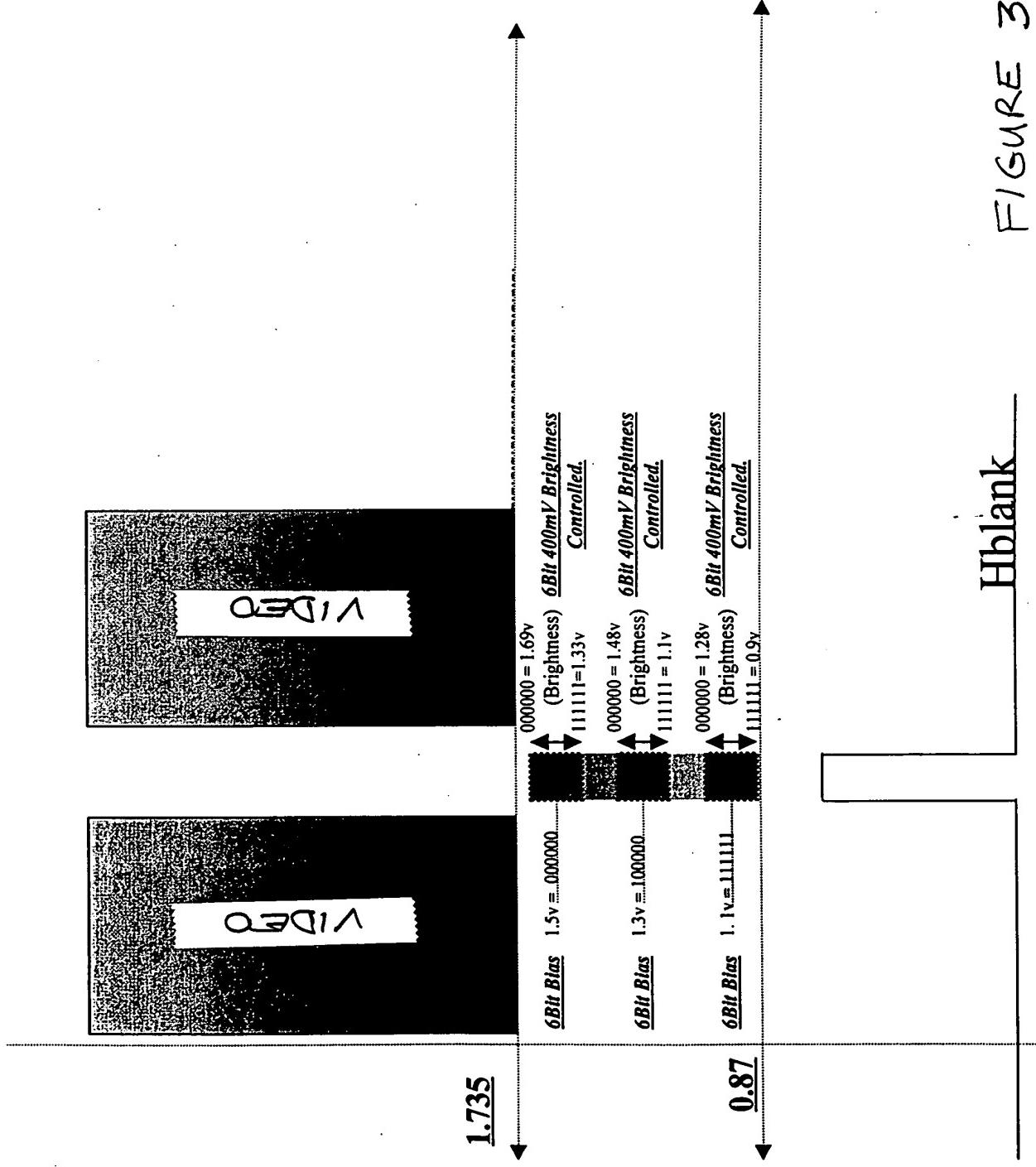
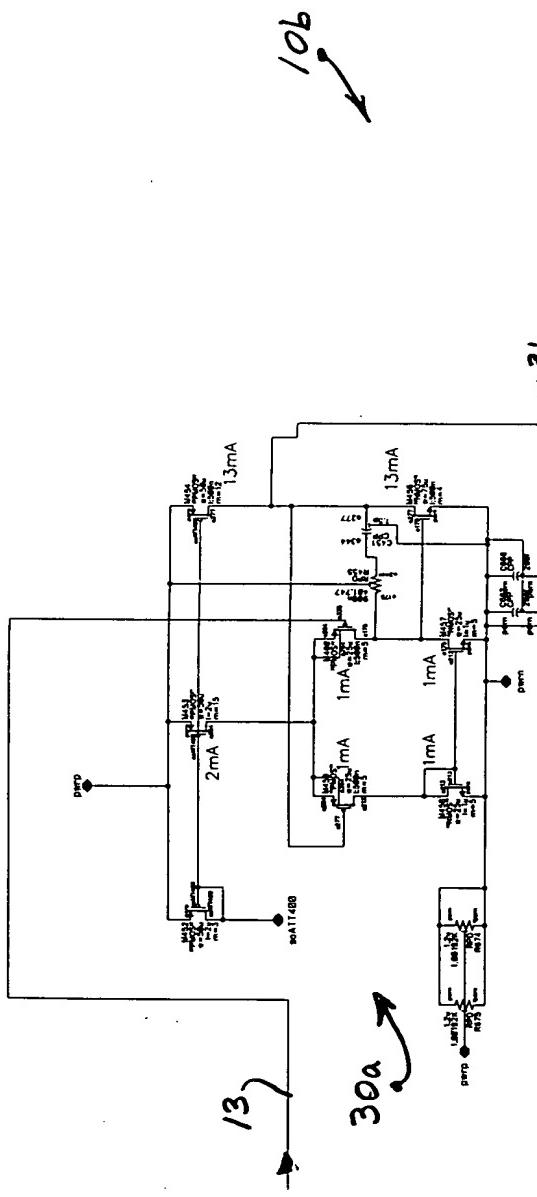


FIGURE 33



32a

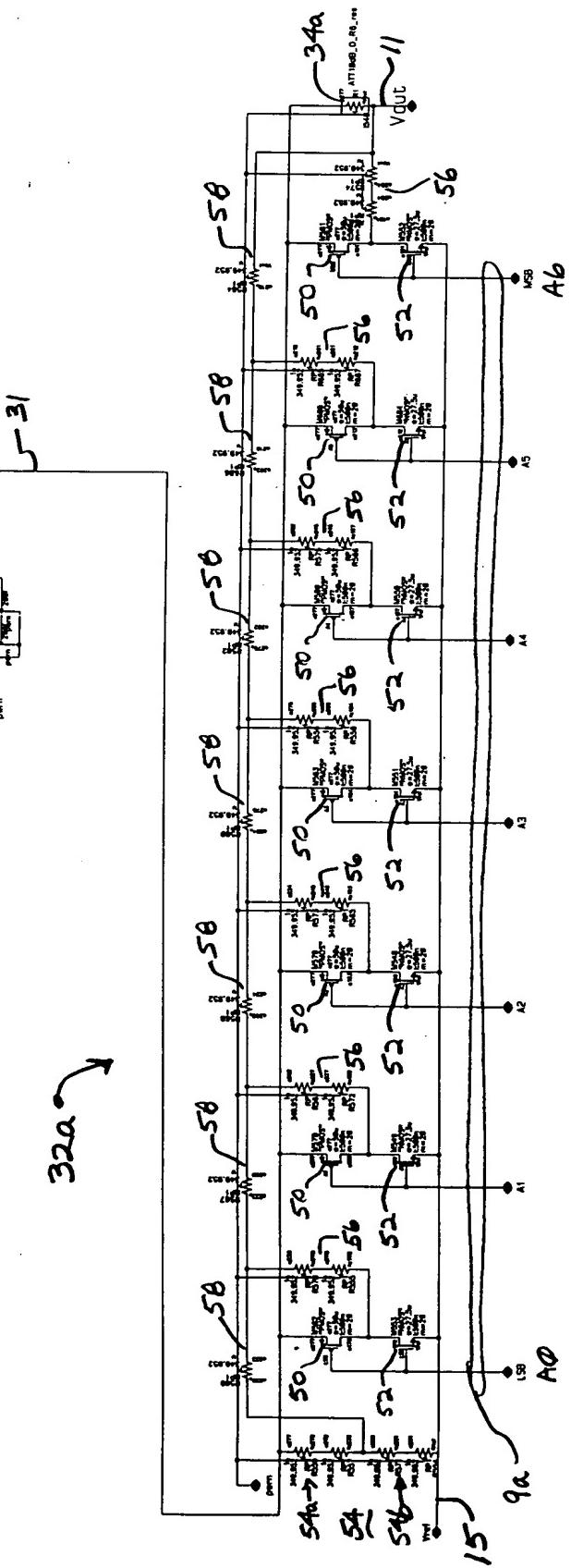
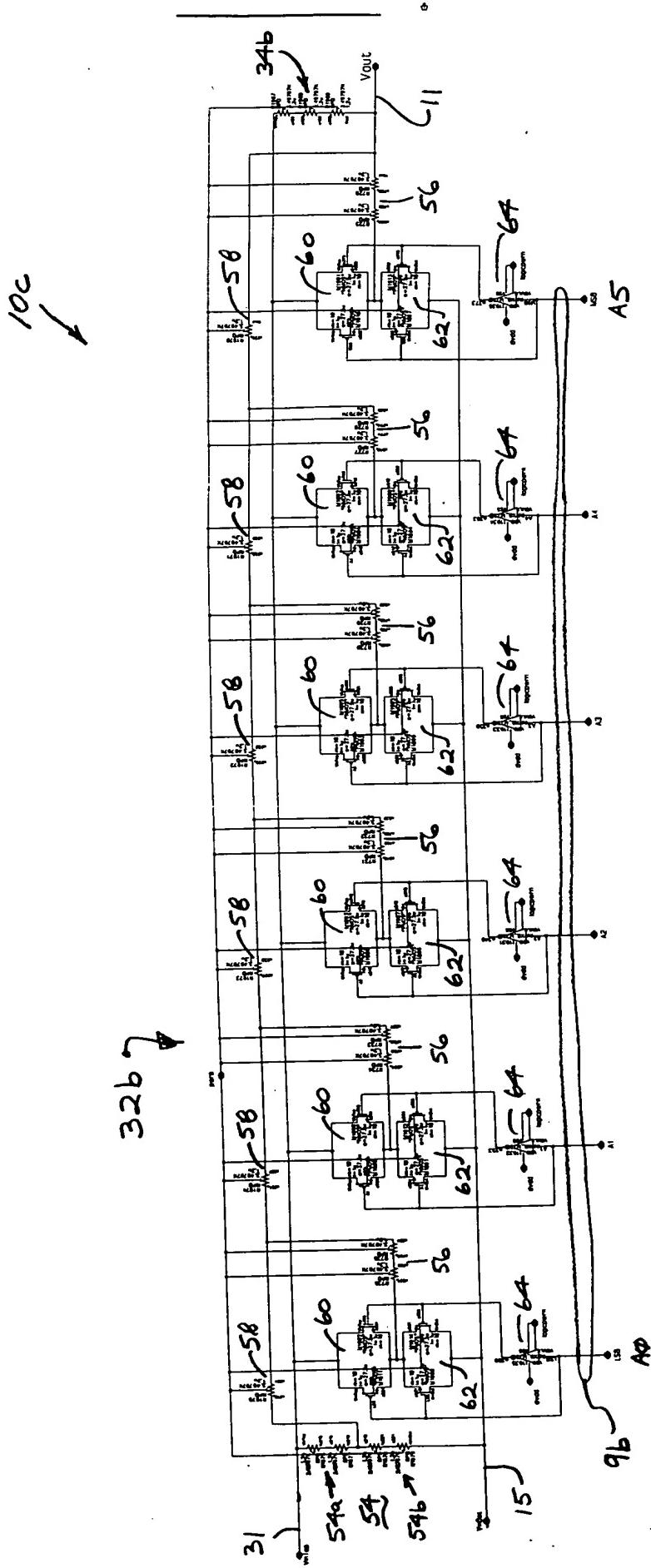


FIGURE 35



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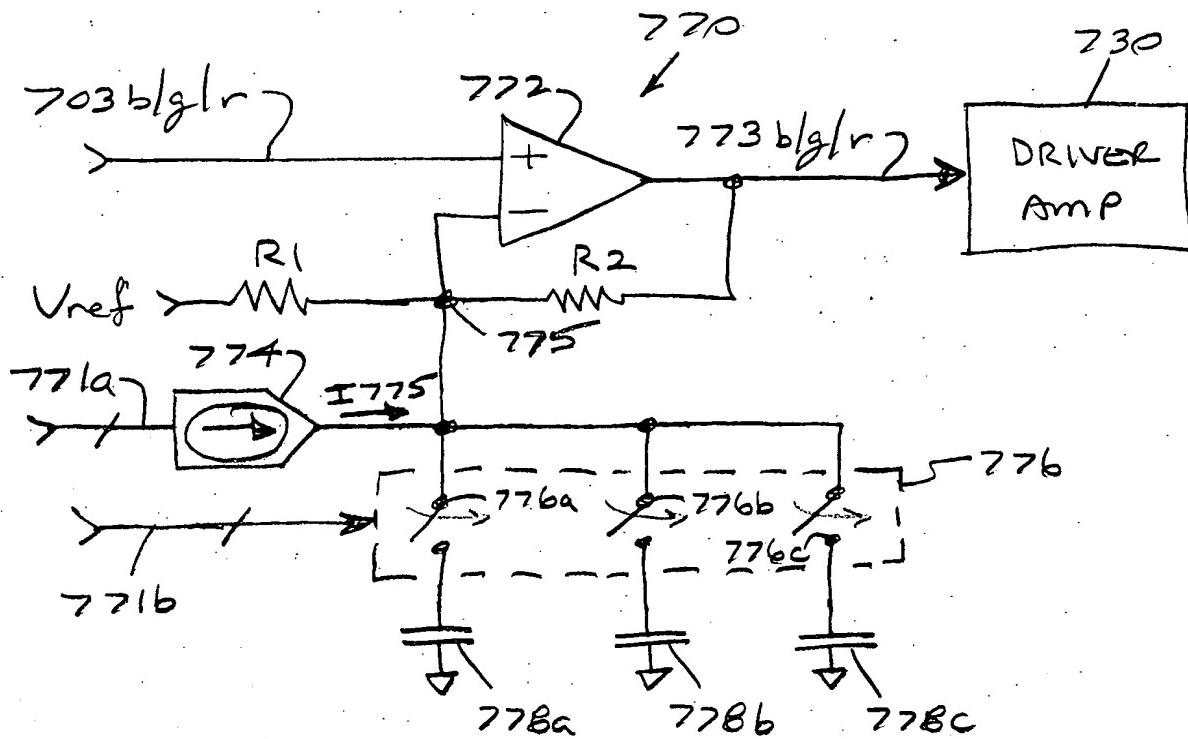


FIGURE 36

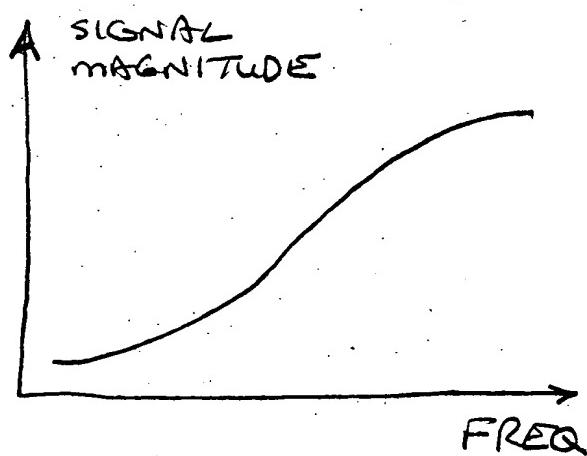


FIGURE 37

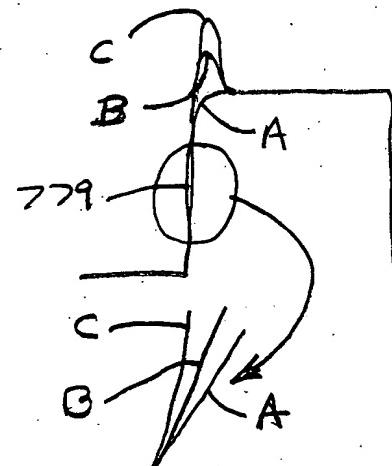


FIGURE 38